ESC N8A-E12

7U Rackmount Server
User Guide

E22682
First Edition
March 2024

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Safety information

Electrical Safety

- Before installing or removing signal cables, ensure that the power cables for the system unit and all attached devices are unplugged.
- To prevent electrical shock hazard, disconnect the power cable from the electrical outlet before relocating the system.
- When adding or removing any additional devices to or from the system, ensure that the power cables for the devices are unplugged before the signal cables are connected. If possible, disconnect all power cables from the existing system before you add a device.
- If the power supply is broken, do not try to fix it by yourself. Contact a qualified service technician or your dealer.

Operation Safety

- Any mechanical operation on this server must be conducted by certified or experienced engineers.
- Before operating the server, carefully read all the manuals included with the server package.
- Before using the server, ensure all cables are correctly connected and the power cables are not damaged. If any damage is detected, contact your dealer as soon as possible.
- To avoid short circuits, keep paper clips, screws, and staples away from connectors, slots, sockets and circuitry.
- Avoid dust, humidity, and temperature extremes. Place the server on a stable surface.

This product is equipped with a three-wire power cable and plug for the user's safety. Use the power cable with a properly grounded electrical outlet to avoid electrical shock.

Restricted Access Location

This product is intended for installation only in a Computer Room where:

- Access can only be gained by SERVICE PERSONS or by USERS who have been instructed about the reasons for the restrictions applied to the location and about any precautions that shall be taken.
- Access is through the use of a TOOL, or other means of security, and is controlled by the authority responsible for the location.

Heavy System

CAUTION! This server system is heavy. Ask for assistance when moving or carrying the system.

About this guide

Audience

This user guide is intended for system integrators and experienced users with at least basic knowledge of configuring a server.

Contents

This guide contains the following parts:

1. **Chapter 1: Product Introduction**

   This chapter describes the general features of the server. It includes sections on front panel and rear panel specifications.

2. **Chapter 2: Hardware Setup**

   This chapter lists the hardware setup procedures that you have to perform when installing or removing system components.

3. **Chapter 3: Motherboard Information**

   This chapter gives information about the motherboard that comes with the server. This chapter includes the motherboard layout, jumper settings, and connector locations.

4. **Chapter 4: BIOS Setup**

   This chapter tells how to change system settings through the BIOS Setup menus and describes the BIOS parameters.

Conventions

To ensure that you perform certain tasks properly, take note of the following symbols used throughout this manual.

DANGER/WARNING: Information to prevent injury to yourself when trying to complete a task.

CAUTION: Information to prevent damage to the components when trying to complete a task.

IMPORTANT: Instructions that you MUST follow to complete a task.

NOTE: Tips and additional information to help you complete a task.

Typography

Bold text	Indicates a menu or an item to select.
Italics	Used to emphasize a word or a phrase.
<Key>	Keys enclosed in the less-than and greater-than sign means that you must press the enclosed key. Example: <Enter> means that you must press the Enter or Return key.
<Key1>+<Key2>+<Key3>	If you must press two or more keys simultaneously, the key names are linked with a plus sign (+). Example: <Ctrl>+<Alt>+<Del>
`Command`	Means that you must type the command exactly as shown, then supply the required item or value enclosed in brackets. Example: At the command prompt, type the command line: `format A:/S`

References

Refer to the following sources for additional information, and for product and software updates.

1. **ASUS Control Center (ACC) user guide**

   This manual tells how to set up and use the proprietary ASUS server management utility.

2. **ASUS websites**

   The ASUS websites provide updated information for all ASUS hardware and software products. Visit https://www.asus.com for more information.

1 Product Introduction

This chapter describes the general features of the server. It includes sections on front panel and rear panel specifications.

1.1 System package contents

Check your system package for the following items.

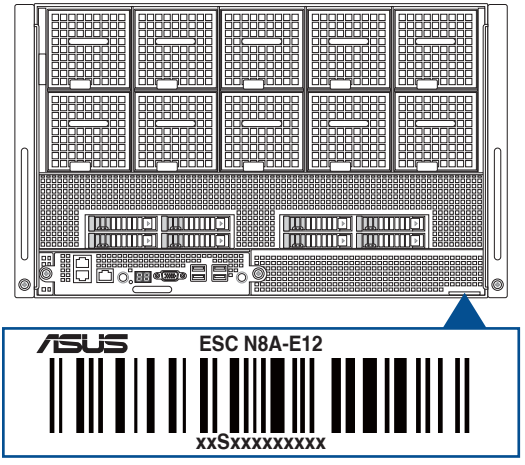
	ESC N8A-E12
Chassis	ASUS 7U rackmount chassis
Motherboard	ASUS K14PN-D24 server board
Accessory box	1 x ACC instruction card 1 x ASMB11 instruction card 6 x AC power cables 2 x CPU heatsink
Optional items	1 x Rail kit 1 x RAID card cable kit

- If any of the above items is damaged or missing, contact your retailer.
- Optional items come bundled if you selected them when purchasing the system and cannot be bought separately.

1.2 Serial number label

Before requesting support from the ASUS Technical Support team, you must take note of the product's serial number containing 12 characters, such as xxSxxxxxxxxx. See the figure below.

With the correct serial number of the product, ASUS Technical Support team members can then offer a quicker and satisfying solution to your problems.



The serial number is printed on the Asset tag.

1.3 System specifications

The ASUS ESC N8A-E12 server features the ASUS K14PN-D24 server board. The server supports AMD EPYC™ 9004 series processors plus other latest technologies through the chipsets onboard.

Model name		ESC N8A-E12
Motherboard		K14PN-D24
Processor support		2 x Socket SP5 (LGA 6096) AMD EPYC™ 9004 series processors (Up to 400W)
Core logic		System on Chip (SoC)
Memory	Total slots	24 (12-channel per CPU, 12 DIMM per CPU)
	Capacity	Maximum up to 3TB per CPU socket (DDR5)
	Memory type	DDR5 4800 RDIMM / 3DS RDIMM * **Refer to ASUS server AVL for the latest update**
	Memory size	128GB, 96GB, 64GB, 32GB, 16GB (RDIMM) 128GB (3DS RDIMM) * **Refer to ASUS server AVL for the latest update**
Expansion slots	Total PCI/PCIe slots	12
	PCIe slot type	**Rear:** - 8 x PCIe (Gen5 x16 link, LP, PCIe switch direct) - 1 x PCIe (Gen5 x16 link, FHHL, CPU1 direct) - 1 x PCIe (Gen5 x16 link, FHHL, CPU2 direct) - 1 x PCIe (Gen5 x8 link, FHHL, CPU2 direct) - 1 x PCIe (Gen5 x8 link, FHHL, CPU2 direct)
	M.2	2 x M.2 socket (Gen5 x4 link, CPU1, up to 2280)
	microSD card slot	1 x for BMC log
	SATA/SAS controller	**Optional kit(s):** - Broadcom 9560-16i MegaRAID card - Broadcom 9670W-16i MegaRAID card

(continued on the next page)

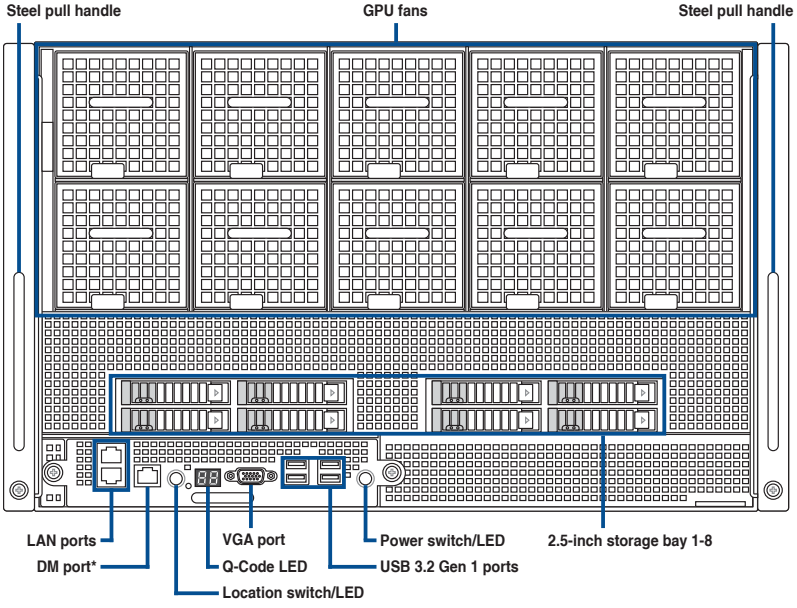
Model name		ESC N8A-E12	
Storage bays	Storage bays	8 x 2.5-inch Front Hot-swap Storage Bays - PCIe switch direct: 8 x NVMe 2 x 2.5-inch Rear Hot-swap Storage Bays - CPU1 direct: 2 x NVMe/SATA*/SAS* * **SATA/SAS support requires an optional HBA/RAID card**	
	Midplane connectors	4 x EXAMAX	
	Backplane connectors	1 x MCIO 1 x Mini SAS HD	
	Default cables	1 x MCIO cable 1 x Mini SAS HD cable	
Networking	LAN	2 x Dual Port Intel® X710-AT2 10GbE LAN controller 1 x Management Port	
VGA		Aspeed AST2600 64MB	
Graphic		HGX H100 8 GPUs with NVLink NVSwitch	
Front I/O		4 x USB 3.2 Gen1 ports 1 x VGA port 2 x RJ-45 LAN ports 1 x RJ-45 management LAN port	
Switch/LED		**Front:** 1 x Power button/LED 1 x Location button/LED 1 x Q-Code/Port 80 LED 2 x LAN LED	**Rear:** 1 x Power button/LED 1 x Location button/LED
Security options		TPM-SPI / PFR (optional)	
OS support		Windows Server, RedHat® Enterprise Linux, CentOS, Ubuntu, VMWare * **Refer to https://www.asus.com/event/Server/OS_support_list/OS.html for the latest supported OS list**	
Management solution	Out of Band remote hardware	Onboard ASMB11-iKVM	
	Software	ASUS Control Center	

(continued on the next page)

Model name	ESC N8A-E12
Regulatory compliance	BSMI, CB, CE, FCC, KCC (Class A)
Dimensions (HH x WW x DD)	885mm x 447 mm x 306.65 mm (7U)
Net weight	99kg (excluding CPU, DRAM, and HDD)
Gross weight	154kg (including packaging, excluding CPU, DRAM, and HDD)
Power supply / Power rating	4+2 or 3+3 3000W 80 PLUS Titanium power supply Rating: 200-220 Vac, 15.8A (x6), 50-60Hz
Environment	Operating temperature: 10°C ~ 35°C Operating temperature with BlueField-3: 10°C ~ 30°C Non-operating temperature: -40°C ~ 70°C Non-operating humidity: 20% ~ 90% (Non-condensing)

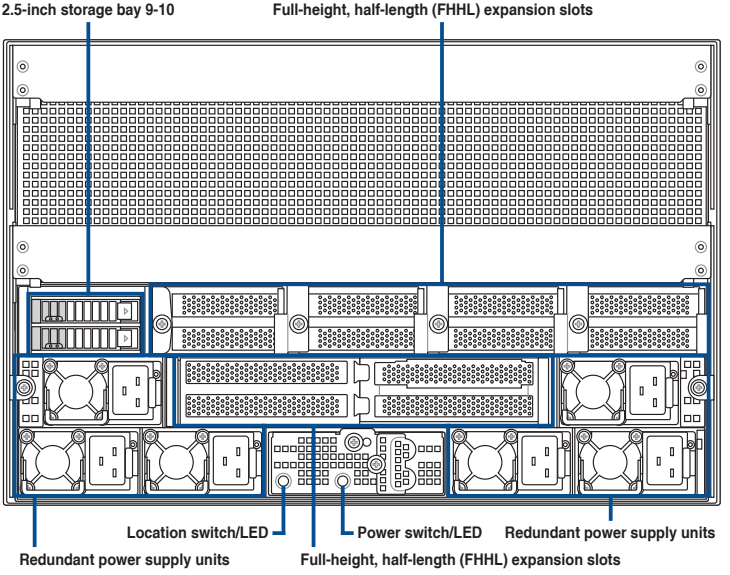
Specifications are subject to change without notice.

1.4 Front panel features



The DM (Dedicated Management) port is for ASUS ASMB11-iKVM only.

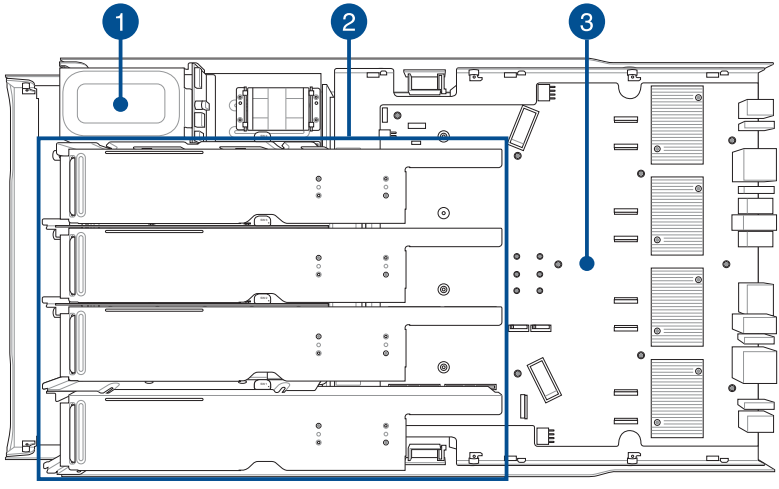
1.5 Rear panel features



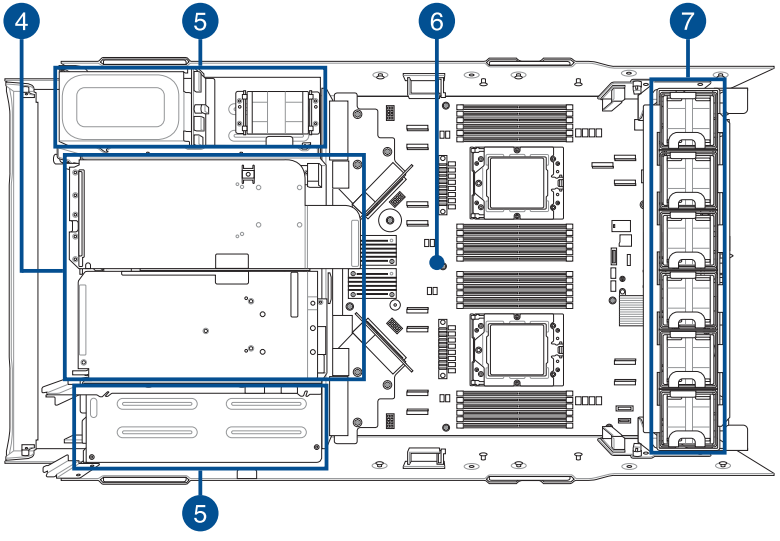
1.6 Internal features

The barebone server includes the basic components as shown.

Upper level (with PCIe switchboard and FHHL PCIe expansion card brackets)



Lower level (without PCIe switchboard and FHHL PCIe expansion card brackets)



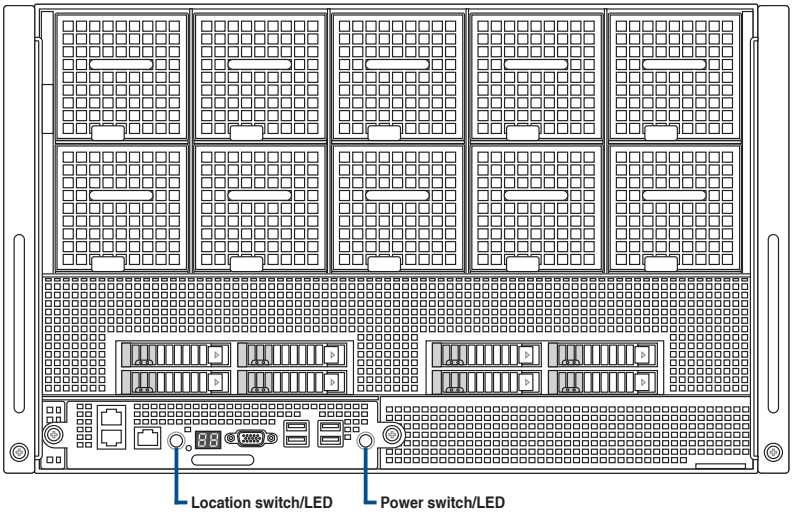
1. 2.5-inch storage bays
2. PCIe expansion card brackets with full-height, half-length expansion slots
3. PCIe switchboard
4. PCIe expansion card brackets with full-height, half-length expansion slots
5. Redundant power supply units
6. ASUS K14PN-D24 server board
7. System fans

A protection film is pre-attached to the front cover before shipping. Remove the protection film before turning on the system for sufficient heat dissipation.

WARNING
HAZARDOUS MOVING PARTS
KEEP FINGERS AND OTHER BODY PARTS AWAY

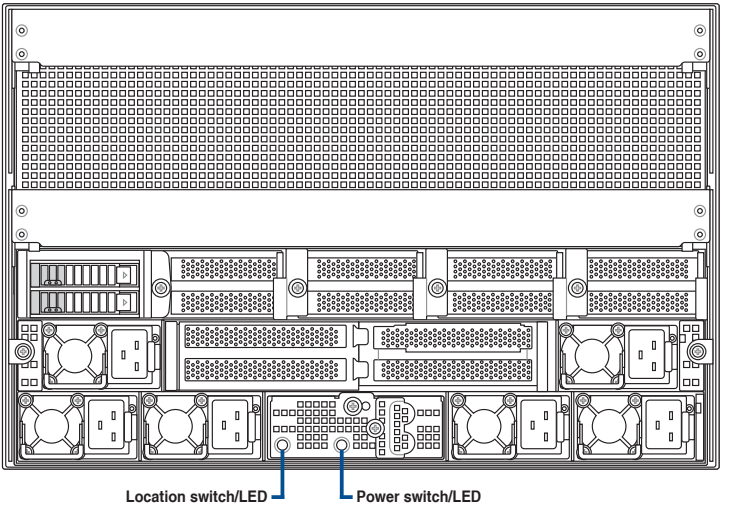
1.7 LED information

1.7.1 Front panel LEDs



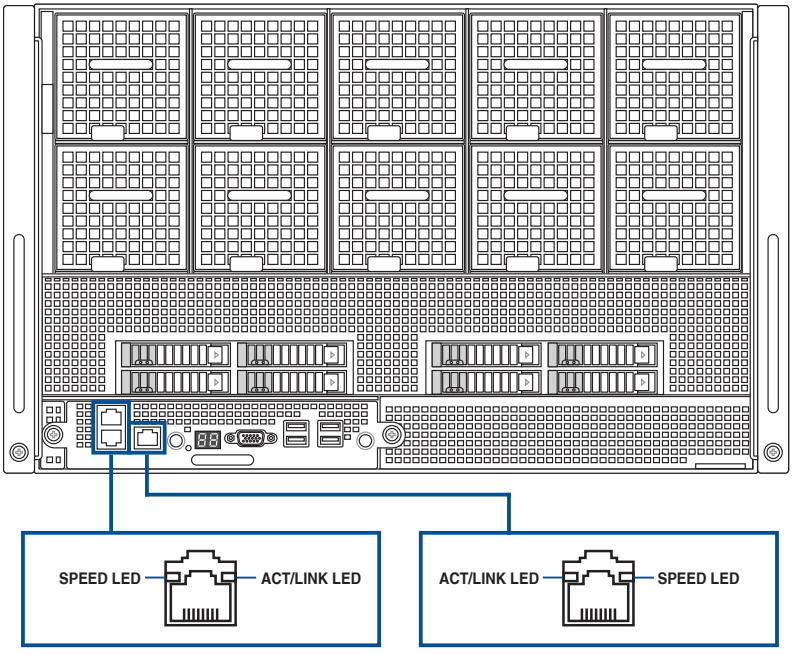
LED	Status	Description
Power LED	ON	System power is on
Location LED	OFF	System is normal; no incoming event
	ON	Received user command to locate the system

1.7.2 Rear panel LEDs



LED	Status	Description
Power LED	ON	System power is on
Location LED	OFF	System is normal; no incoming event
	ON	Received user command to locate the system

1.7.3 LAN (RJ-45) LEDs



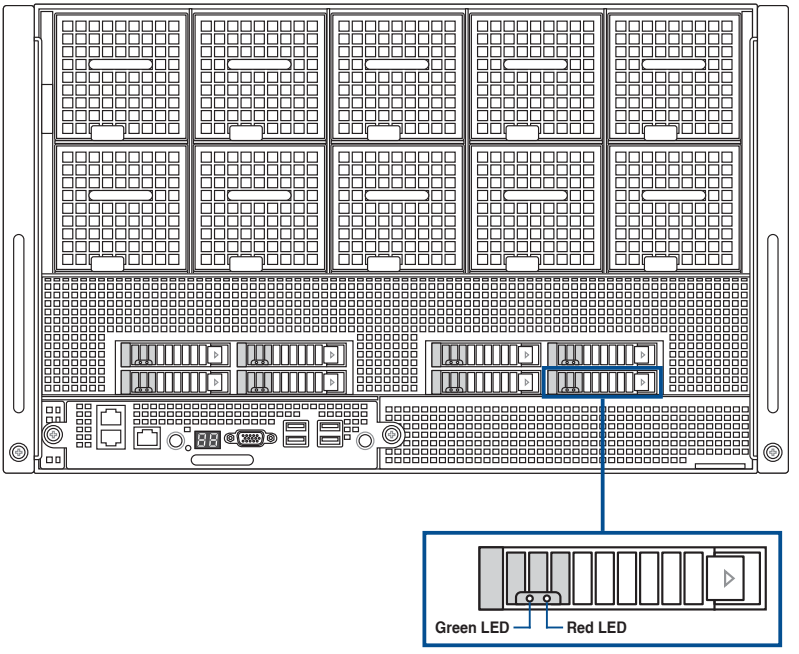
Intel® X710-AT2 10GbE LAN port LEDs

SPEED LED		ACT/LINK LED	
Status	Description	Status	Description
OFF	100 Mbps connection	OFF	No link
YELLOW	1 Gbps connection	GREEN	Linked
GREEN	10 Gbps connection	BLINKING	Data activity

Dedicated Management LAN port (DM_LAN1) LED indications

SPEED LED		ACT/LINK LED	
Status	Description	Status	Description
OFF	10 Mbps connection	OFF	No link
YELLOW	100 Mbps connection	YELLOW	Linked
GREEN	1 Gbps connection	BLINKING	Data activity

1.7.4 Storage device status LEDs



Storage Device LED Description		
Status (RED)	ON	Storage device has failed
	Blinking	RAID rebuilding or locating
Activity (GREEN)	ON	Storage device power ON
	Blinking	SATA/SAS/NVMe storage device reading or writing data
	OFF	Storage device not found

2 Hardware Setup

This chapter lists the hardware setup procedures that you have to perform when installing or removing system components.

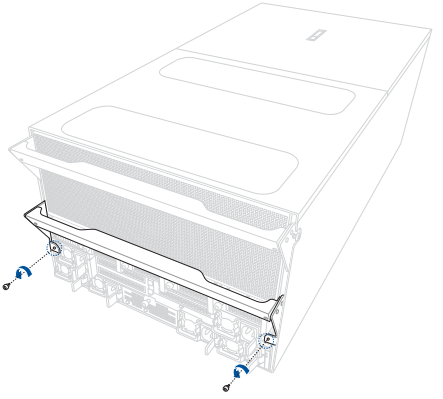
2.1 Server tray

A protection film is pre-attached to the system cover before shipping. Please remove the protection film before turning on the system for proper heat dissipation.

2.1.1 Removing the server tray

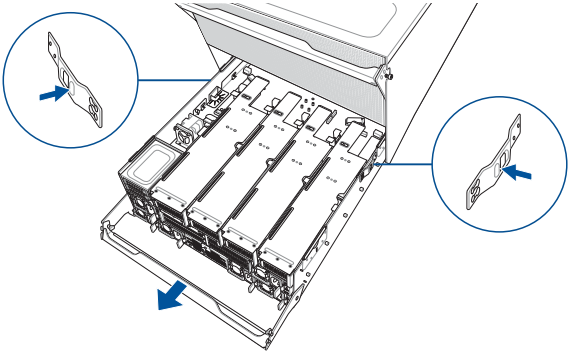
1. Remove the two thumbscrews from the server tray handle.

Keep the thumbscrews in a safe place for later use.



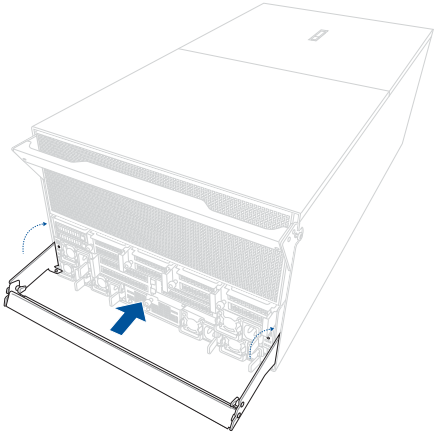
2. Pull the server tray handle downwards and slowly pull the server tray halfway out of the server chassis, then press both latches inwards and fully remove the server tray.

Slowly pull the server tray with both hands.

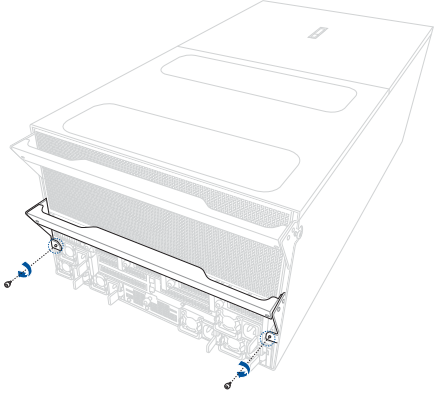


2.1.2 Installing the server tray

1. Align and push the server tray all the way into the server tray slot, then push the server tray handle upwards.



2. Secure the server tray handle using the thumbscrews, then tighten the thumbscrews by hand until the screw thread is no longer visible.

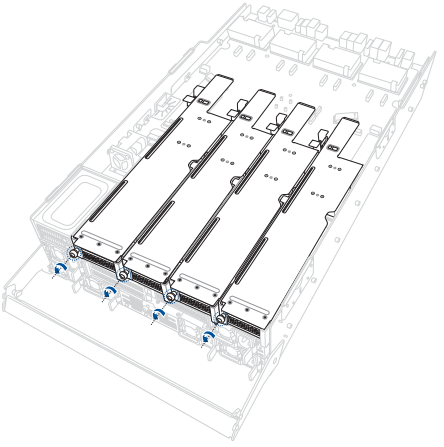


3. Use a tool to fully secure the thumbscrews with a torque value of 5 kgf-cm.

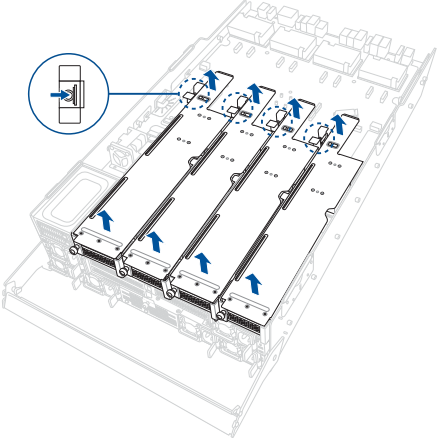
2.2 PCIe expansion card brackets

2.2.1 Removing the upper PCIe expansion card brackets

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Loosen the thumbscrew on the PCIe expansion card bracket.

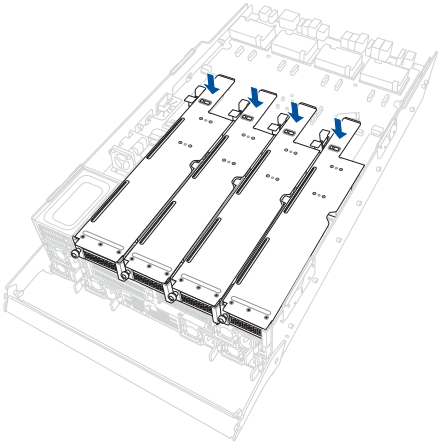


3. Press the latch on the PCIe expansion card bracket and lift the bracket upwards to remove it from the server tray.

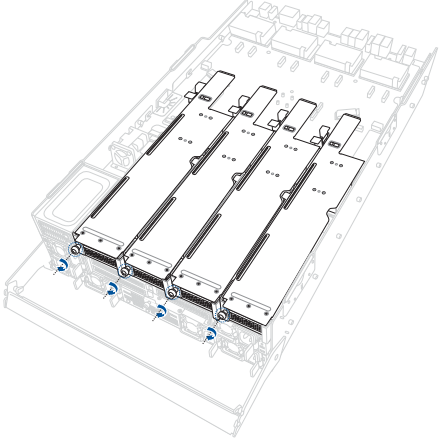


2.2.2 Installing the upper PCIe expansion card brackets

1. Align and install the PCIe expansion card bracket into the server tray, then firmly press down on the indicated area of the PCIe expansion card bracket until a click is heard.

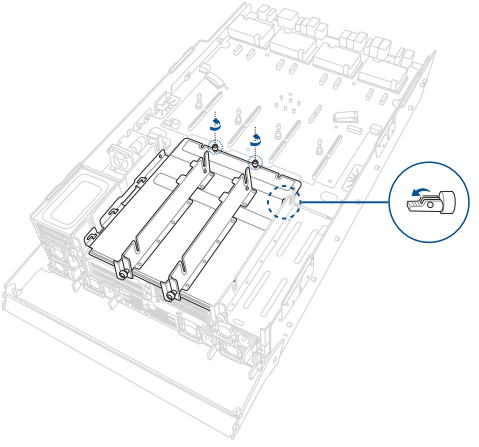


2. Secure the bracket using the thumbscrew.

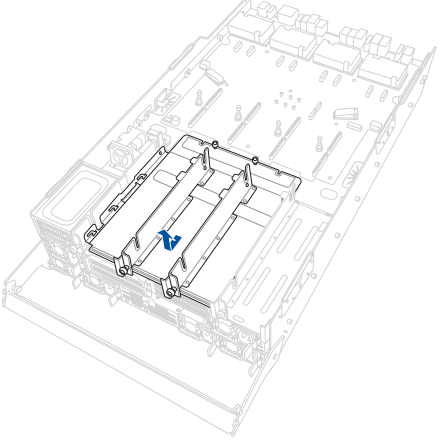


2.2.3 Removing the lower PCIe expansion card brackets

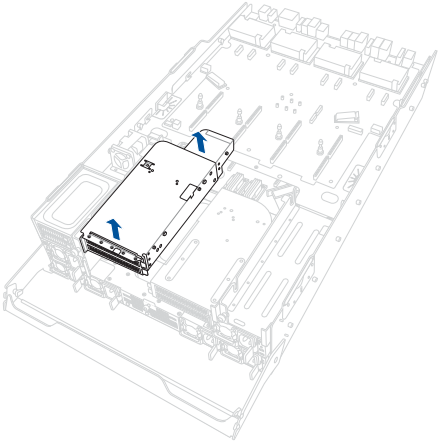
1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Loosen the two thumbscrews and disengage the latch on the PCIe expansion card tray.



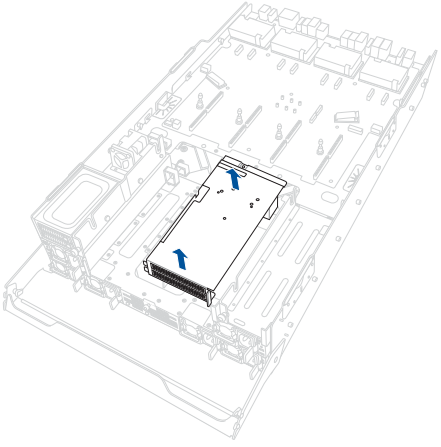
4. Slide the PCIe expansion card tray towards the rear of the server tray and pull upwards to remove it.



5. Pull the right PCIe expansion card bracket upwards to remove it from the server tray.

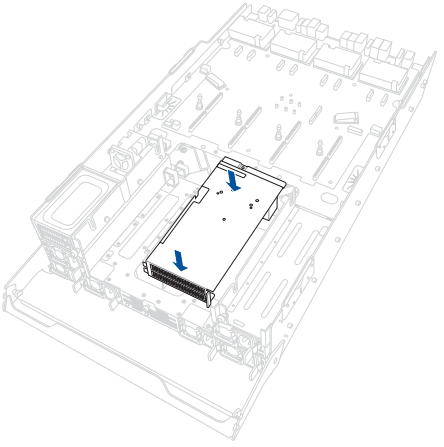


6. Pull the left PCIe expansion card bracket upwards to remove it from the server tray.

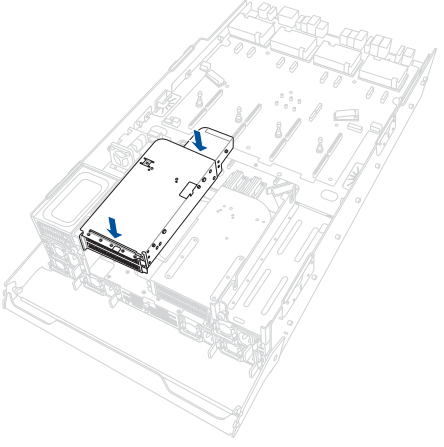


2.2.4 Installing the lower PCIe expansion card brackets

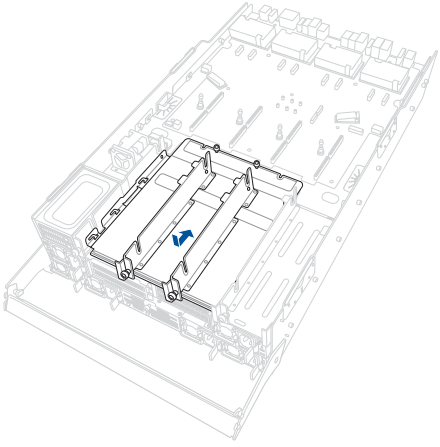
1. Align and install the left PCIe expansion card bracket into the server tray.



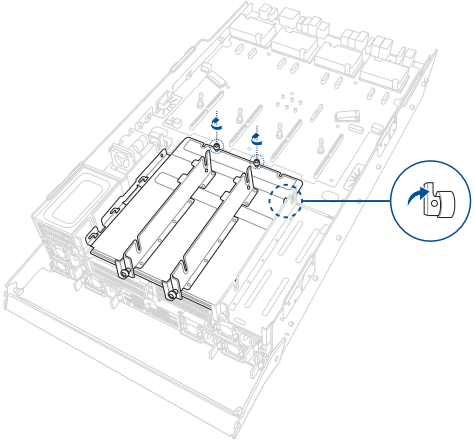
2. Align and install the right PCIe expansion card bracket into the server tray.



3. Align and install the PCIe expansion card tray into the server tray.

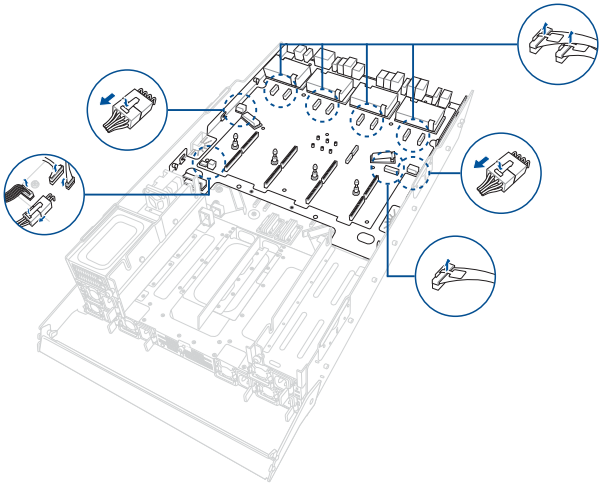


4. Return the latch to the locked position, then use the two thumbscrews to secure the PCIe expansion card tray.

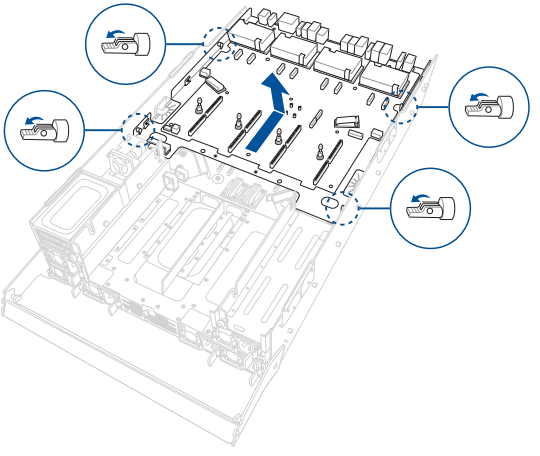


2.2.5 Removing the PCIe switchboard

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.
4. Disconnect the cables from the PCIe switchboard.

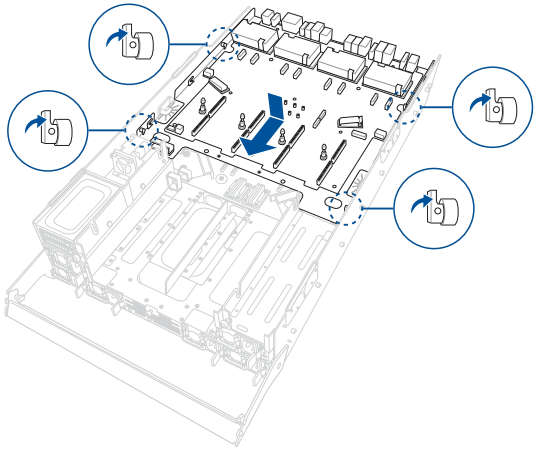


5. Disengage the latches on the PCIe switchboard, then slide the PCIe switchboard towards the front of the server tray and pull upwards to remove it.

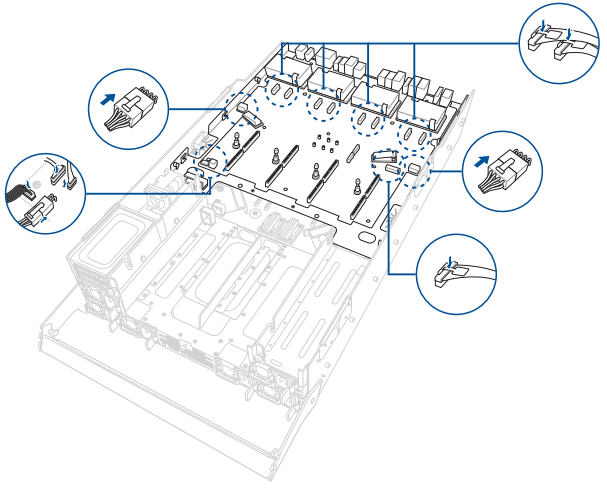


2.2.6 Installing the PCIe switchboard

1. Align and install the PCIe switchboard into the server tray, then return the latches to the locked position to secure it.



2. Reconnect the cables to the PCIe switchboard.

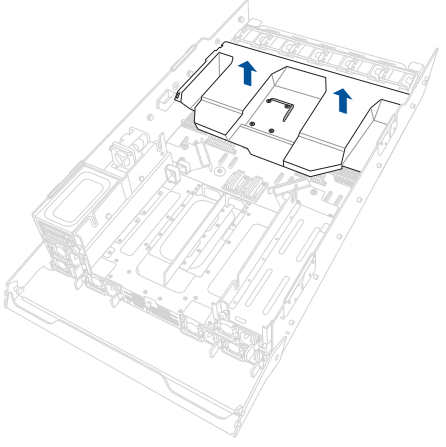


2.3 Central Processing Unit (CPU)

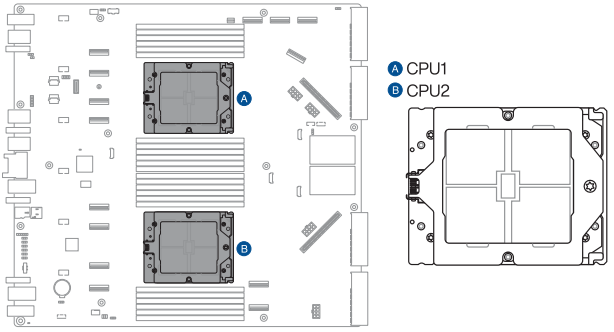
The motherboard comes with two surface mount Socket SP5 sockets designed for AMD EPYC™ 9004 Series CPUs.

- Upon purchase of the motherboard, ensure that the PnP cap is on the socket and the socket contacts are not bent. Contact your retailer immediately if the PnP cap is missing, or if you see any damage to the PnP cap/socket contacts/motherboard components. ASUS will shoulder the cost of repair only if the damage is shipment/transit-related.
- Keep the cap after installing the motherboard. ASUS will process Return Merchandise Authorization (RMA) requests only if the motherboard comes with the cap on the socket.
- The product warranty does not cover damage to the socket contacts resulting from incorrect CPU installation/removal, or misplacement/loss/incorrect removal of the PnP cap.

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.
4. Remove the PCIe switchboard from the server tray. For more information, see the **Removing the PCIe switchboard** section.
5. Lift the air duct to remove it from the server tray.



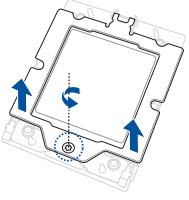
6. Locate the CPU socket on the motherboard.



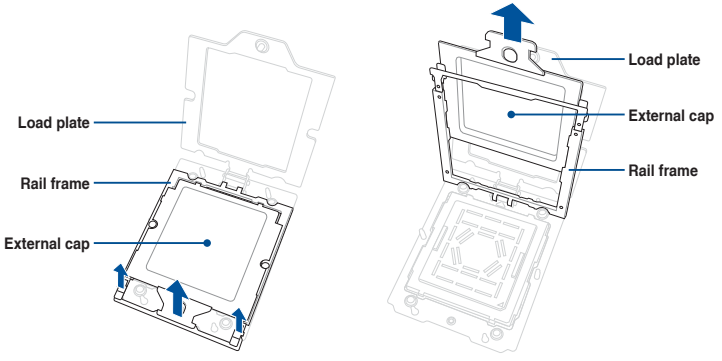
K14PN-D24 CPU Socket SP5 LGA 6096

7. Loosen the screw on the socket to open the load plate.

A T20 screwdriver with a torque value of 13.5±1.0 kgf-cm is recommended.

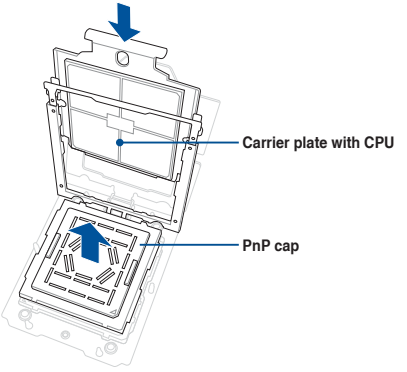


8. Lift open the rail frame, then slide the external cap out of the rail frame.

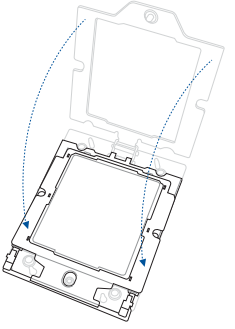


9. Slide the carrier frame with CPU into the rail frame, then remove the PnP cap.

The carrier frame with CPU fits in only one correct orientation. DO NOT force the carrier frame with CPU into the rail frame.

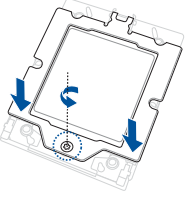


10. Gently close the rail frame just enough to let it sit on top of the CPU socket.

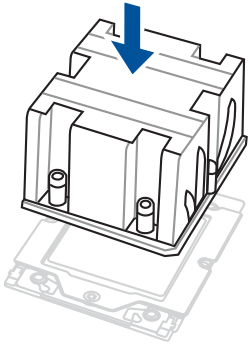


11. Close the load plate just enough to let it sit on top of the CPU, then secure the load plate using the screw on the socket.

A T20 screwdriver with a torque value of 13.5±1.0 kgf-cm is recommended.

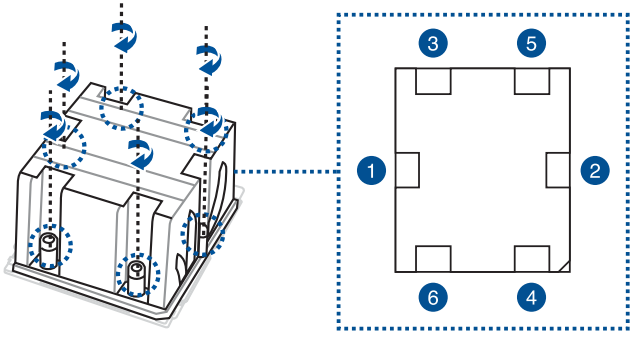


12. Place the heatsink on the CPU socket and make sure the heatsink screws are aligned with the CPU socket.

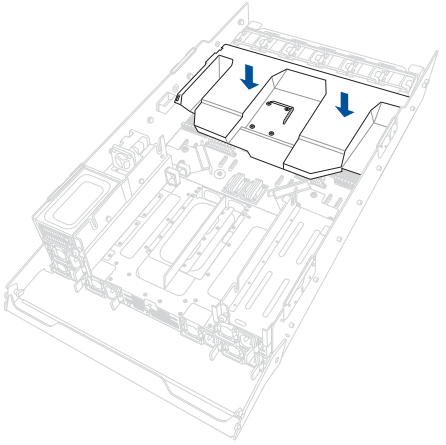


13. Partially tighten each of the six screws with a screwdriver in the order shown both in the illustration and on the heatsink just enough to attach the heatsink to the motherboard. When the six screws are attached, tighten them one by one in the same order to completely secure the heatsink.

- A T20 screwdriver with a torque value of 13.5±1.0kg-cm is recommended.
- To remove the heatsink, loosen the screws in the reverse order.



14. Align and insert the air duct into the server tray.

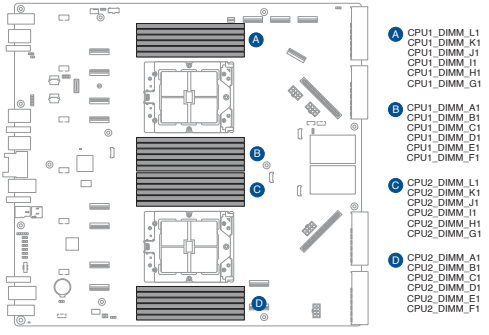


2.4 System memory

2.4.1 Overview

The motherboard comes with twenty four Double Data Rate 5 (DDR5) Dual Inline Memory Modules (DIMM) sockets.

The figure illustrates the location of the DDR5 DIMM sockets:



K14PN-D24 288-pin DDR5 DIMM slots

2.4.2 Memory configurations

You may install 16GB, 32GB, 64GB, 96GB, 128GB RDIMMs or 128GB 3DS RDIMMs into the DIMM sockets using the recommended memory configurations in this section.

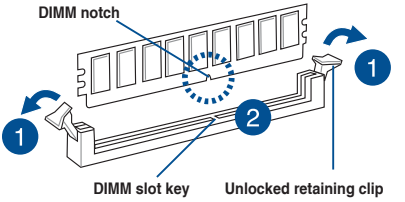
- Refer to ASUS Server AVL for the updated list of compatible DIMMs.
- Always install DIMMs with the same CAS latency. For optimum compatibility, it is recommended that you obtain memory modules from the same vendor.

Recommended memory configuration							
	2 DIMMs	4 DIMMs	8 DIMMs	12 DIMMs	16 DIMMs	20 DIMMs	24 DIMMs
CPU1_DIMM_A1	•	•	•	•	•	•	•
CPU1_DIMM_B1				•	•	•	•
CPU1_DIMM_C1			•	•	•	•	•
CPU1_DIMM_D1						•	•
CPU1_DIMM_E1					•	•	•
CPU1_DIMM_F1							•
CPU1_DIMM_G1		•	•	•	•	•	•
CPU1_DIMM_H1				•	•	•	•
CPU1_DIMM_I1			•	•	•	•	•
CPU1_DIMM_J1						•	•
CPU1_DIMM_K1					•	•	•
CPU1_DIMM_L1							•
CPU2_DIMM_A1	•	•	•	•	•	•	•
CPU2_DIMM_B1				•	•	•	•
CPU2_DIMM_C1			•	•	•	•	•
CPU2_DIMM_D1						•	•
CPU2_DIMM_E1					•	•	•
CPU2_DIMM_F1							•
CPU2_DIMM_G1		•	•	•	•	•	•
CPU2_DIMM_H1				•	•	•	•
CPU2_DIMM_I1			•	•	•	•	•
CPU2_DIMM_J1						•	•
CPU2_DIMM_K1					•	•	•
CPU2_DIMM_L1							•

2.4.3 Installing a DIMM

Ensure to unplug the power supply before adding or removing DIMMs or other system components. Failure to do so may cause severe damage to both the motherboard and the components.

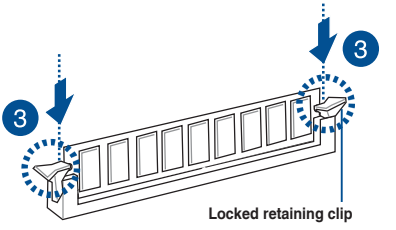
1. Unlock a DIMM socket by pressing the retaining clips outward.
2. Align a DIMM on the socket such that the notch on the DIMM matches the DIMM slot key on the socket.



A DIMM is keyed with a notch so that it fits in only one direction. DO NOT force a DIMM into a socket in the wrong direction to avoid damaging the DIMM.

3. Hold the DIMM by both of its ends, then insert the DIMM vertically into the socket. Apply force to both ends of the DIMM simultaneously until the retaining clips snap back into place.

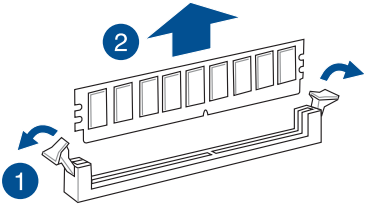
   Ensure that the DIMM is sitting firmly in the DIMM slot.



Always insert the DIMM into the socket VERTICALLY to prevent DIMM notch damage.

2.4.4 Removing a DIMM

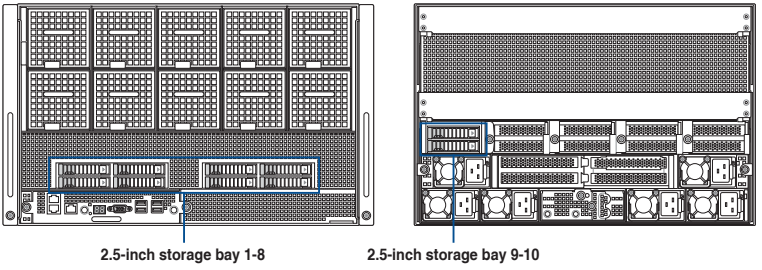
1. Simultaneously press the retaining clips outward to unlock the DIMM.
2. Remove the DIMM from the socket.



Support the DIMM lightly with your fingers when pressing the retaining clips. The DIMM might get damaged when it springs out with extra force.

2.5 Storage devices

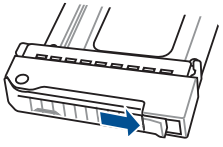
The system supports up to ten (10) 2.5-inch NVMe storage devices and up to two (2) 2.5-inch hot-swap NVMe/SATA/SAS storage devices. Storage devices installed on storage device trays connect to the motherboard via the NVMe/SATA/SAS backplane (SATA/SAS storage devices require an optional HBA/RAID card).



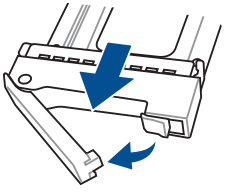
2.5.1 Installing a 2.5-inch storage device

To install a 2.5-inch storage device:

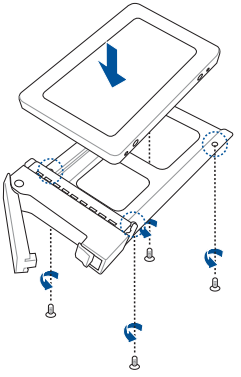
1. Press the spring lock to release the tray lever and partially eject the tray from the bay.



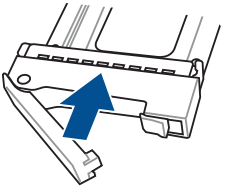
2. Firmly hold the tray lever and pull the storage device tray out of the bay.



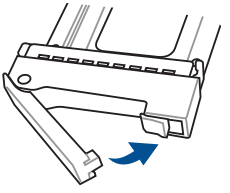
3. Prepare the 2.5-inch storage device and the bundled set of screws.
4. Place the 2.5-inch storage device onto the tray, then secure it with four screws.



5. Carefully insert the tray and push it all the way into the bay.



6. Lock the tray lever to secure the storage device tray in place.



7. Repeat steps 1 to 6 to install additional 2.5-inch storage devices.

2.6 Expansion slots

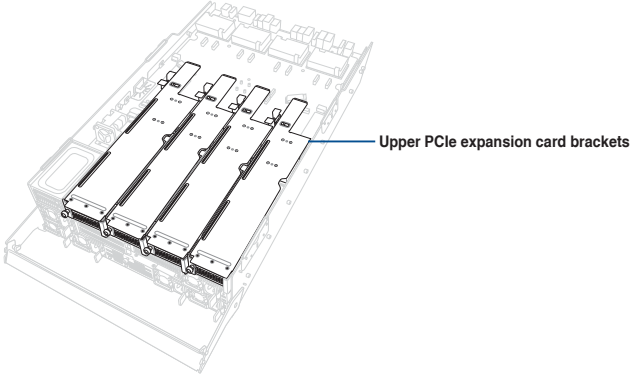
Ensure to unplug the power cord before adding or removing expansion cards. Failure to do so may cause you physical injury and damage motherboard components.

2.6.1 Installing an expansion card to the upper PCIe expansion card brackets

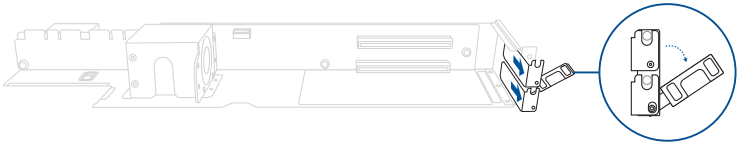
The server system comes pre-installed with four upper PCIe expansion card brackets that each support two x16 slots (Gen5 x16 link) for installing half-height, half-length PCIe expansion cards.

To install a PCIe expansion card to the upper PCIe expansion card bracket:

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Place the upper PCIe expansion card bracket on a level surface.

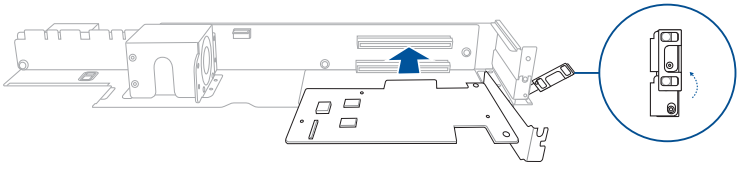


4. Push the slot cover lock outwards, then remove the PCIe slot cover.



5. Insert the expansion card into the PCIe slot and ensure that it is securely seated, then push the slot cover lock inwards to secure the expansion card.

Before installing an expansion card, read the documentation that came with it and ensure that the proper hardware settings are configured.



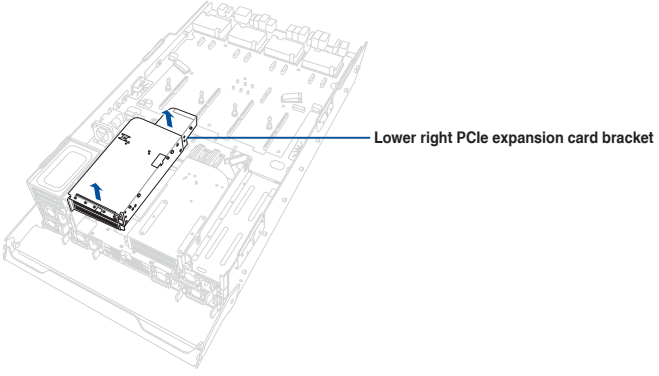
2.6.2 Installing an expansion card to the lower PCIe expansion card brackets

The server system comes pre-installed with two lower PCIe expansion card brackets that support two x16 slots (Gen5 x16 link) for installing half-height, half-length PCIe expansion cards and one x8 slot (Gen4 x8 link) for installing a RAID card.

The PCIe x8 slot may be unavailable on certain models.

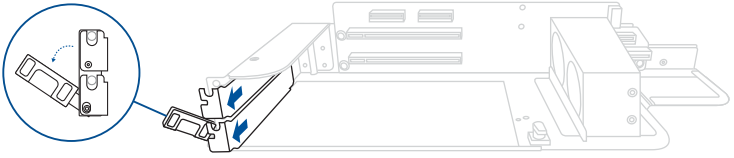
To install a PCIe expansion card to the lower right PCIe expansion card bracket:

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.



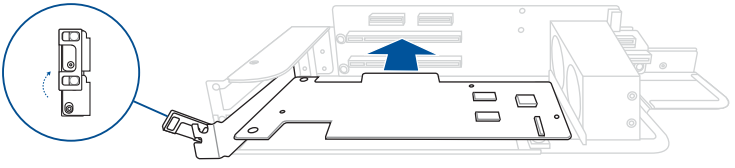
4. Place the lower right PCIe expansion card bracket on a level surface.

5. Push the slot cover lock outwards, then remove the PCIe slot cover.



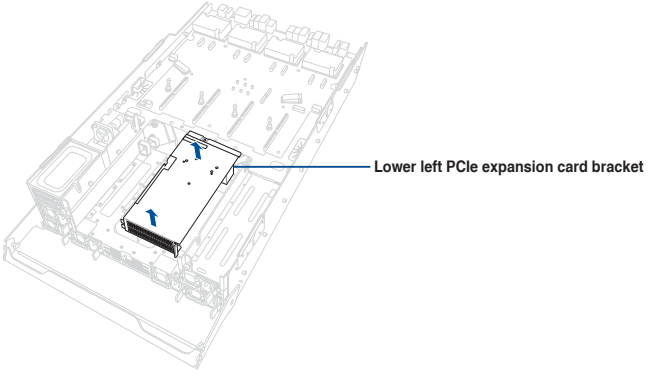
6. Insert the expansion card into the PCIe slot and ensure that it is securely seated, then push the slot cover lock inwards to secure the expansion card.

Before installing an expansion card, read the documentation that came with it and ensure that the proper hardware settings are configured.

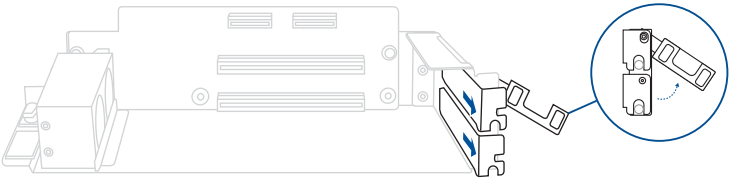


To install a PCIe expansion card to the lower left PCIe expansion card bracket:

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.

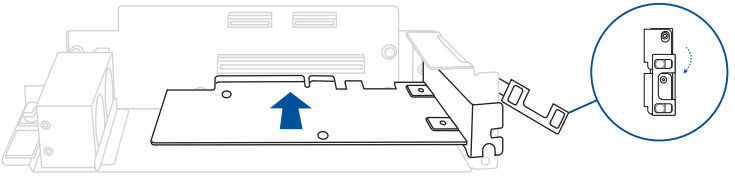


4. Place the lower left PCIe expansion card bracket on a level surface.
5. Push the slot cover lock outwards, then remove the PCIe slot cover.



6. Insert the expansion card into the PCIe slot and ensure that it is securely seated, then push the slot cover lock inwards to secure the expansion card.

Before installing an expansion card, read the documentation that came with it and ensure that the proper hardware settings are configured.

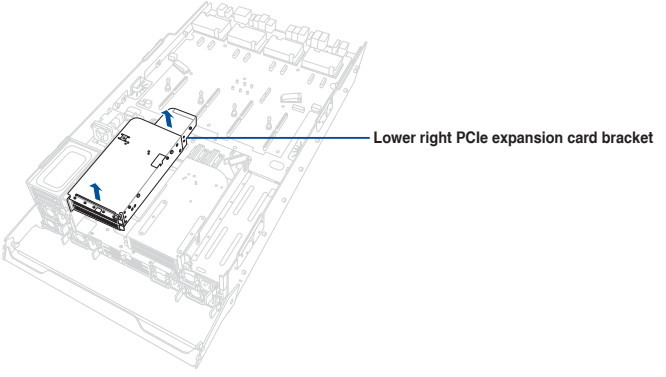


2.6.3 Installing a RAID card

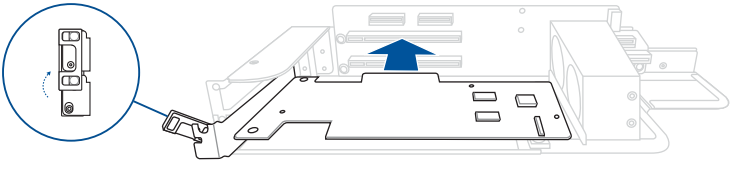
A RAID card can be installed in the PCIe x8 slot on the lower right PCIe expansion card bracket.

The PCIe x8 slot may be unavailable on certain models.

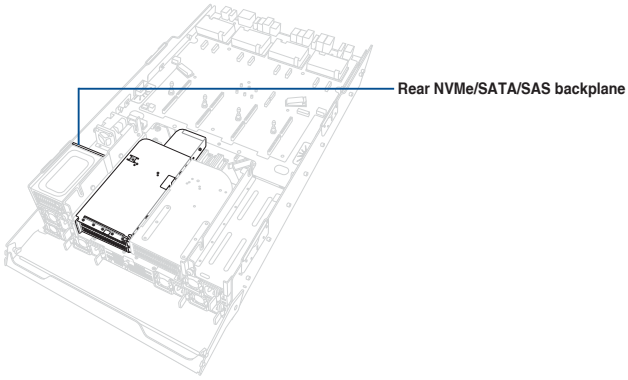
1. Prepare the RAID card.
2. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
3. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
4. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.



5. Place the lower right PCIe expansion card bracket on a level surface.
6. Insert the RAID card into the PCIe slot and ensure that it is securely seated.



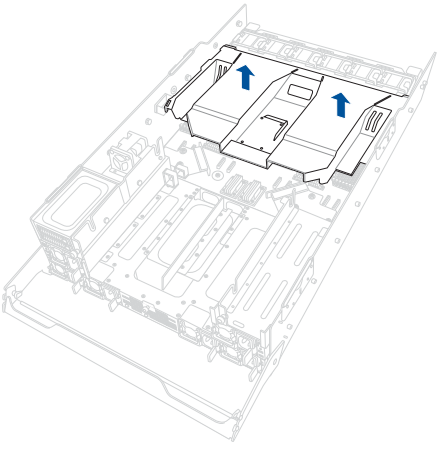
7. Connect the RAID card to the rear NMVe/SATA/SAS backplane.



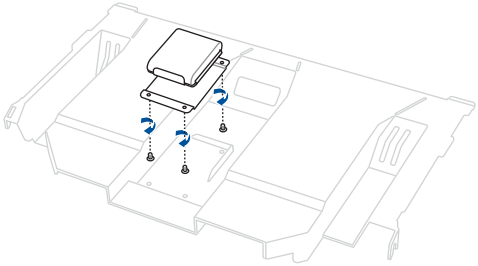
8. (Optional) Refer to the **Installing the Cache Vault Power Module** section to install and connect the Cache Vault Power Module.

2.6.4 Installing the Cache Vault Power Module

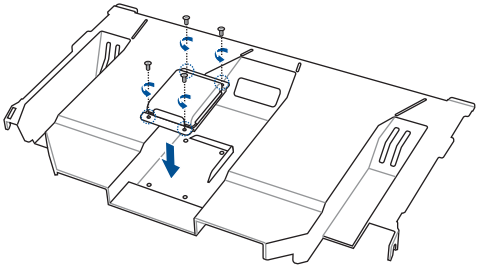
1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.
4. Remove the PCIe switchboard from the server tray. For more information, see the **Removing the PCIe switchboard** section.
5. Lift the air duct to remove it from the server tray.



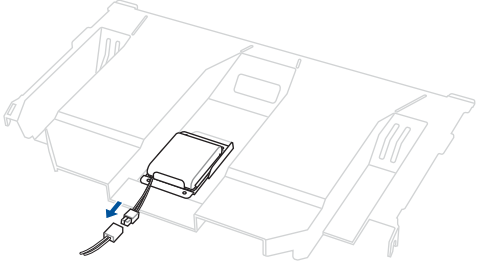
6. Secure the cache vault power module to the bracket with three screws.



7. Install the cache vault power module onto the air duct, then secure the cache vault power module.



8. Connect the cache vault power module to the RAID card.



2.7 Removable/optional components

The following sections describe installation or removal instructions for the following removable/optional components:

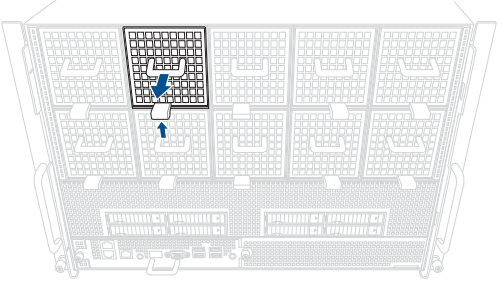
1. GPU fans
2. System fans
3. Redundant power supply units
4. Front bezel (optional)
5. PFR module (optional)
6. Chassis intrusion sensor
7. Rail kit

Ensure that the system is turned off before removing any components.

2.7.1 GPU fans

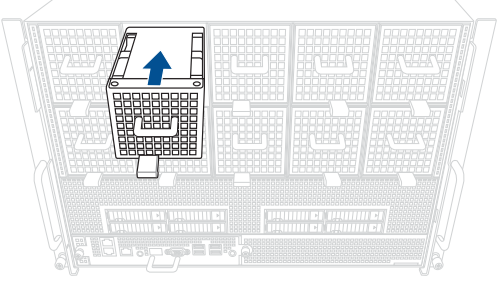
To uninstall a GPU fan:

Press the latch inwards to release the fan, then remove the fan from the fan cage.



To install a GPU fan:

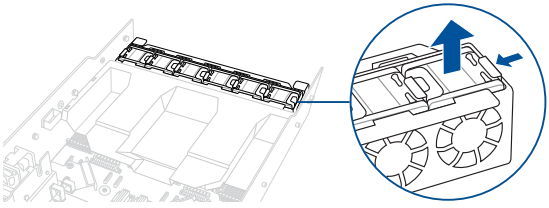
Insert the fan into the fan cage and ensure that it is securely seated.



2.7.2 System fans

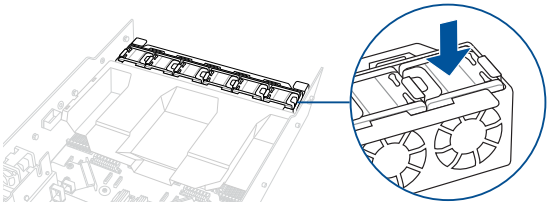
To uninstall a system fan:

1. Remove the server tray from the server chassis. For more information, see the **Removing the server tray** section.
2. Remove the upper PCIe expansion card brackets from the server tray. For more information, see the **Removing the upper PCIe expansion card brackets** section.
3. Remove the lower PCIe expansion card brackets from the server tray. For more information, see the **Removing the lower PCIe expansion card brackets** section.
4. Remove the PCIe switchboard. For more information, see the **Removing the PCIe switchboard** section.
5. Press the latch inwards to release the fan, then remove the fan from the fan cage.



To install a system fan:

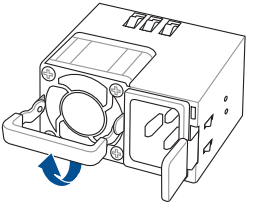
Insert the fan into the fan cage and ensure that it is securely seated.



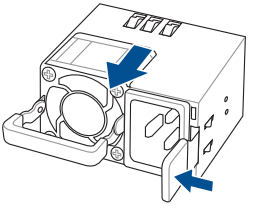
2.7.3 Redundant power supply units

To uninstall a power supply unit (PSU):

1. Lift up the PSU lever.

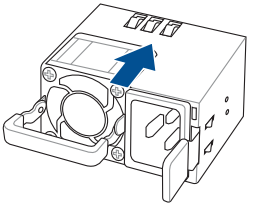


2. Hold the PSU lever and press the PSU latch inwards, then carefully pull the PSU out of the system chassis.



To install a power supply unit (PSU):

Align and install the PSU into the server chassis until it clicks into place.



- The system automatically combines the six power supply modules as a single one.
- To enable the hot-swap feature (redundant mode), keep the total power consumption of the system under the maximum output power of an individual power supply module.

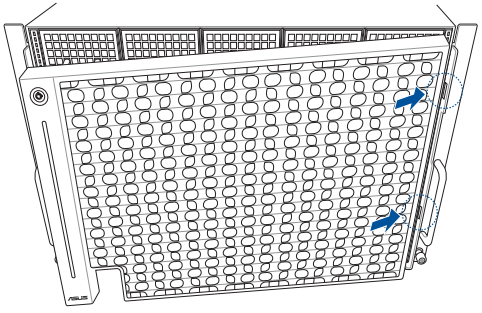
- Always use PSUs with the same wattage and power rating. Combining PSUs with different wattages may yield unstable results and potential damage to your system.
- At least three working power supply units are required in order for the system to boot normally.
- The server rack or external power supply system must supply 200-220V with at least 60A at 50-60Hz. If connected to a power outlet, each power outlet must individually supply at least 16A.
- For a steady power input, use only the power cables that come with the server system package.

2.7.4 Front bezel (optional)

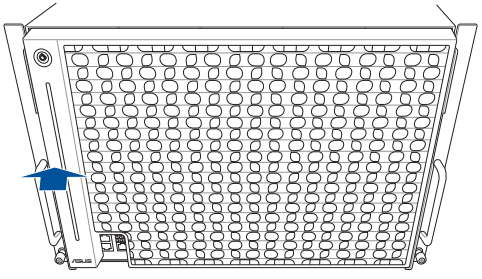
For extra security, a front bezel (purchased separately) can be installed to prevent unauthorized physical access to the hard drives and power button.

To install the front bezel:

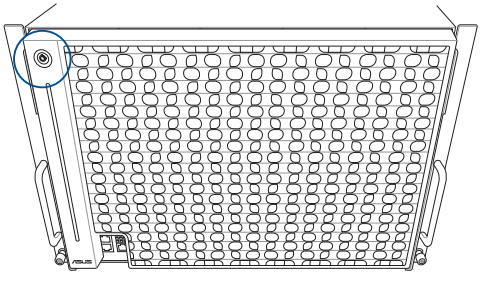
1. Align the two notches on the right side of the front bezel with the corresponding holes on the front panel.



2. Attach the left side of the front bezel to the front panel.

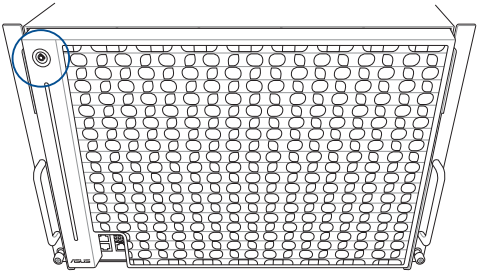


3. (Optional) Lock the front bezel with the bundled key to prevent unauthorized access.

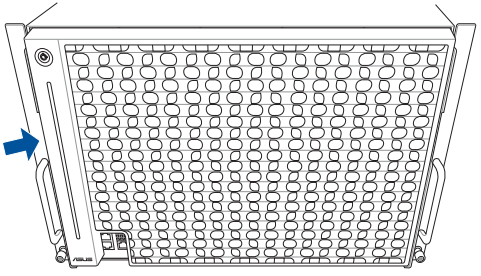


To uninstall the front bezel:

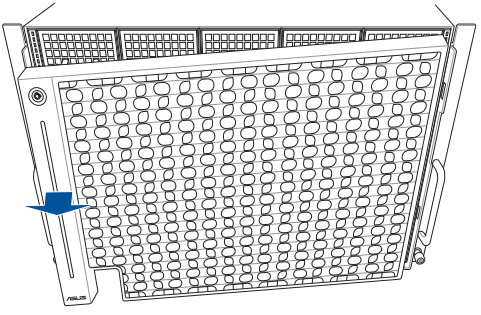
1. Unlock the front bezel with the bundled key, if locked.



2. Press the bezel release latch on the left side of the front bezel.



3. Pull the left side of the front bezel to remove it from the front panel.

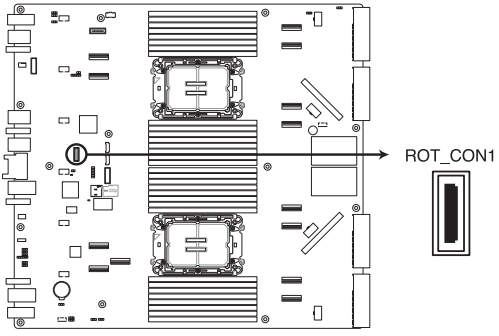


2.7.5 PFR module (optional)

The optional PFR module will come pre-installed on your system and is connected to the PFR module connector on your motherboard.

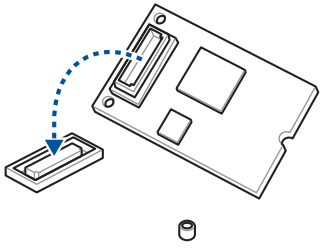
- The illustration below is for reference only.
- For more information or assistance, please refer to www.asus.com.

1. Locate the PFR module connector on your motherboard.

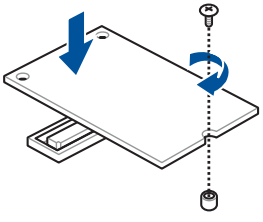


Z13PN-D32 ROT_CON1

2. Align and connect the PFR module to the PFR module connector.



3. Push the PFR module down so that it is seated securely on the PFR module connector, then secure it using a screw.



2.7.6 Chassis intrusion sensor

A chassis intrusion sensor will come pre-installed on your system and is connected to the Chassis Intrusion connector (2-pin INTRUSION1) on the midplane. To disable the chassis intrusion sensor, short the CHASSIS# and GND pins with a jumper cap.

2.7.7 Rail kit

This server system supports the rail kit options listed below. For more information on rail kit installation, refer to corresponding documentation on the ASUS support site or on the official product site for this server system.

- We strongly recommend that at least two able-bodied persons perform the installation of the rail kit.
- We recommend the use of an appropriate lifting tool or device, if necessary.

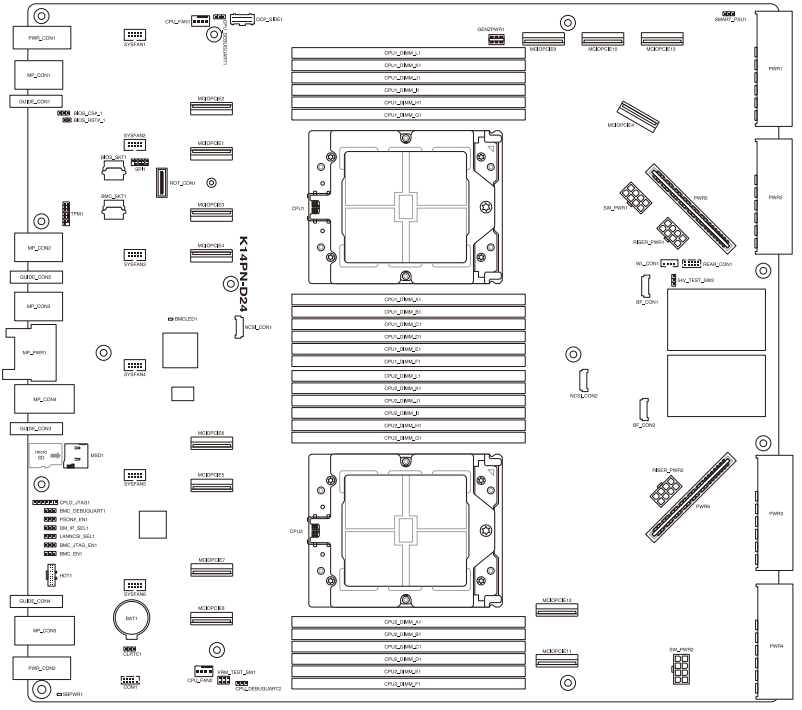
- 2U L-shelf rail kit

Motherboard Information

3

This chapter gives information about the motherboard that comes with the server. This chapter includes the motherboard layout, jumper settings, and connector locations.

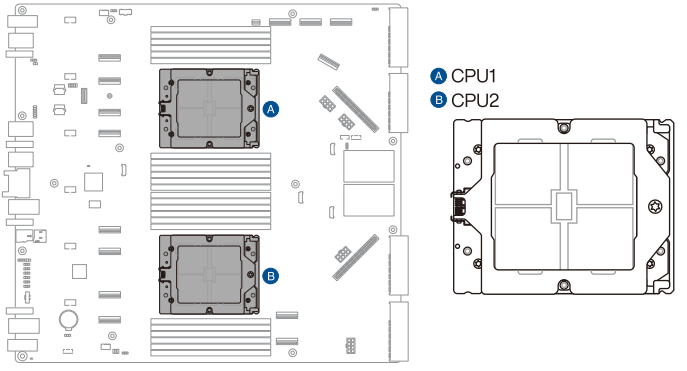
3.1 Motherboard layout



Layout contents

3.2 Central Processing Unit (CPU)

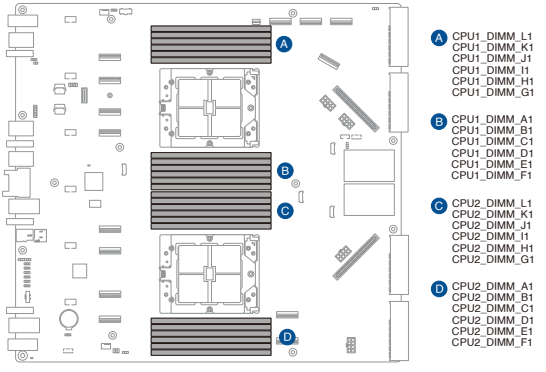
The motherboard comes with two surface mount Socket SP5 sockets designed for AMD EPYC™ 9004 Series CPUs.



K14PN-D24 CPU Socket SP5 LGA 6096

3.3 Dual Inline Memory Module (DIMM)

The motherboard comes with twenty four Double Data Rate 5 (DDR5) Dual Inline Memory Modules (DIMM) sockets.



K14PN-D24 288-pin DDR5 DIMM slots

3.4 Jumpers

1. Clear RTC RAM (3-pin CLRTC1)

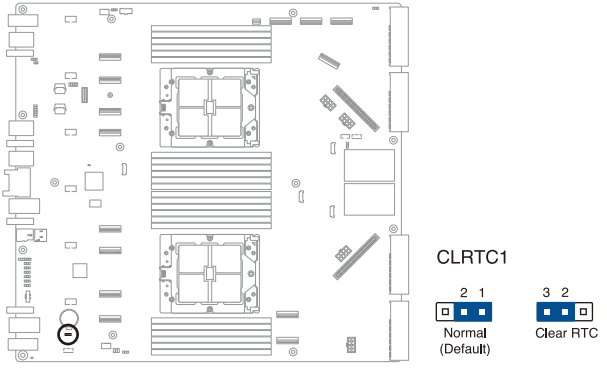
This jumper allows you to clear the CMOS memory system setup parameters by erasing the CMOS Real Time Clock (RTC) RAM data. The onboard button cell battery powers the RAM data in CMOS, which includes system setup information such as system passwords.

To erase the RTC RAM:

1. Turn OFF the computer and unplug the power cord.
2. Move the jumper cap from pins 1–2 (default) to pins 2–3. Keep the cap on pins 2–3 for about 5–10 seconds, then move the cap back to pins 1–2.
3. Plug the power cord and turn ON the computer.
4. Hold down the <Del> key during the boot process and enter BIOS setup to re-enter data.

Except when clearing the RTC RAM, never remove the cap on CLRTC jumper default position. Removing the cap will cause system boot failure!

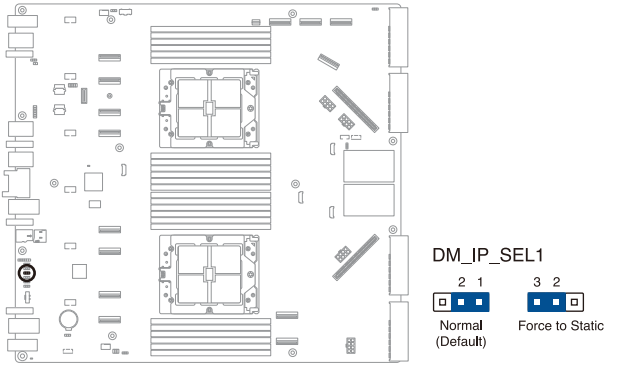
If the steps above do not help, remove the onboard battery and move the jumper again to clear the CMOS RTC RAM data. After the CMOS is cleared, reinstall the battery.



K14PN-D24 CLRTC1

2. DMLAN setting (3-pin DM_IP_SEL1)

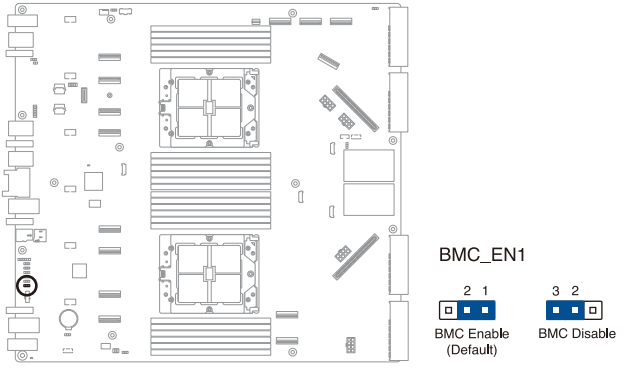
This jumper allows you to select the DMLAN setting. Set to pins 2-3 to force the DMLAN IP to static mode (IP=10.10.10.10, submask=255.255.255.0).



K14PN-D24 DM_IP_SEL1

3. Baseboard Management Controller setting (3-pin BMC_EN1)

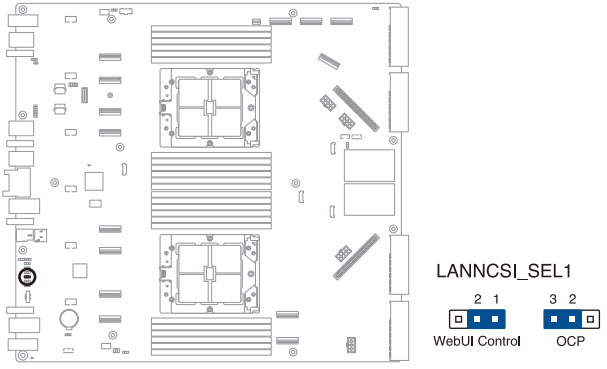
This jumper allows you to enable (default) or disable on-board BMC. Ensure that this BMC jumper to enabled to avoid system fan control and hardware monitor error.



K14PN-D24 BMC_EN1

4. LANNCSI setting (3-pin LANNCSI_SEL1)

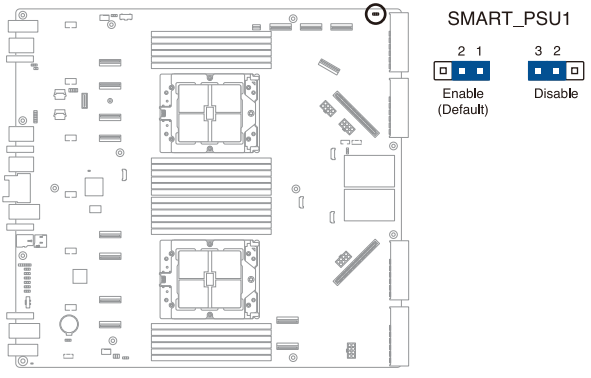
This jumper allows you to select the NCSI device.



K14PN-D24 LANNCSI_SEL1

5. Smart Ride Through (SmaRT) setting (3-pin SMART_PSU1)

This jumper allows you to enable or disable the Smart Ride Through (SmaRT) function. This feature is enabled by default. Set to pins 2-3 to disable it. When enabled, SmaRT allows uninterrupted operation of the system during an AC loss event.

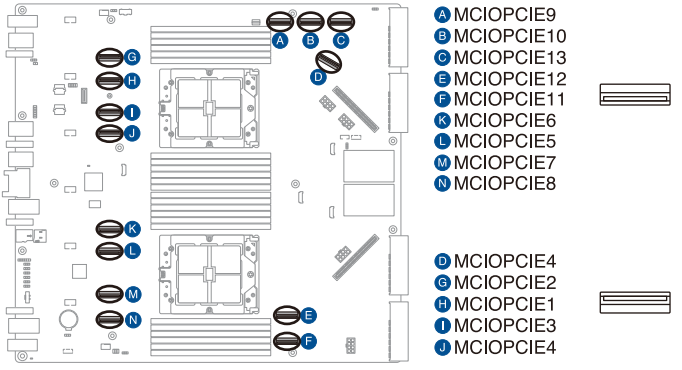


K14PN-D24 SMART_PSU1

3.5 Internal connectors

1. MCIOPCIE connectors (MCIOPCIE1-13)

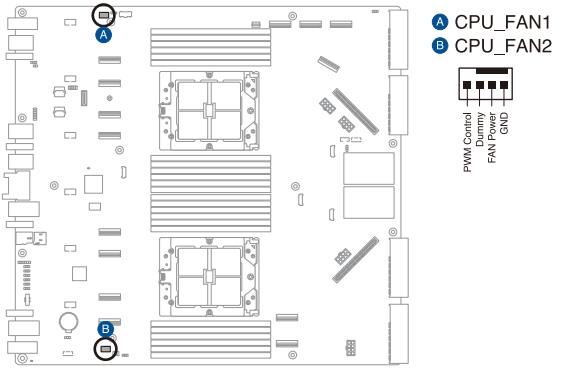
Connects the PCIe signal to the backplane and riser.



K14PN-D24 MCIOPCIE

2. CPU fan connectors (4-pin CPU_FAN1-2)

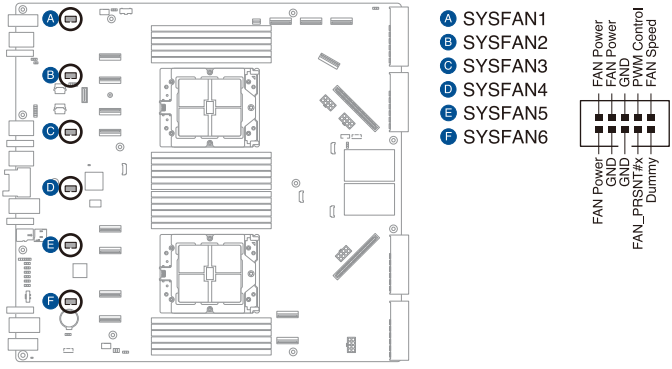
These connectors supply power to CPU fans when installed for testing purposes.



K14PN-D24 FAN connectors

3. System fan connectors (10-pin SYSFAN1-6)

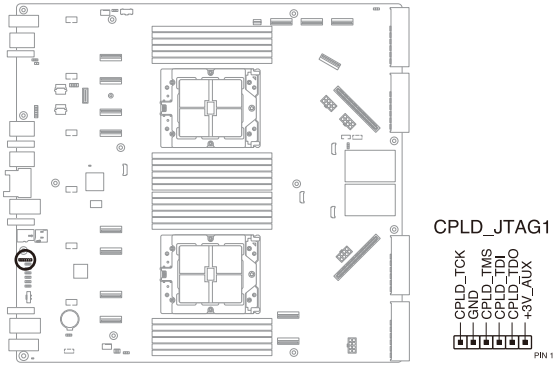
These connectors supply power to the system fans.



K14PN-D24 SYSFAN connectors

4. CPLD JTAG connector (6-pin CPLD_JTAG1)

This connector is used to program the CPLD firmware.



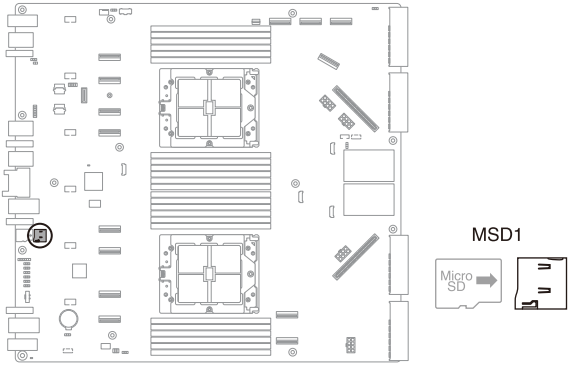
K14PN-D24 CPLD_JTAG1

5. microSD card slot (MSD1)

The microSD card slot allows you to install a microSD memory card v2.00 (SDHC) / v3.00 (SDXC) to log BMC events.

Disconnect all power (including redundant PSUs) from the existing system before you add or remove a memory card, then reboot the system to access the memory card.

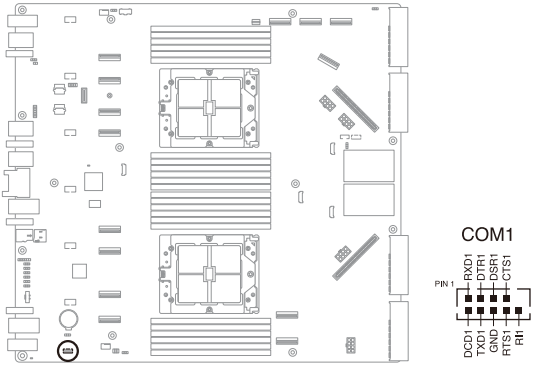
Some memory cards may not be compatible with your motherboard. Ensure that you use only compatible memory cards to prevent loss of data, damage to your device or memory card, or both.



K14PN-D24 MSD1

6. Serial port connector (10-1 pin COM1)

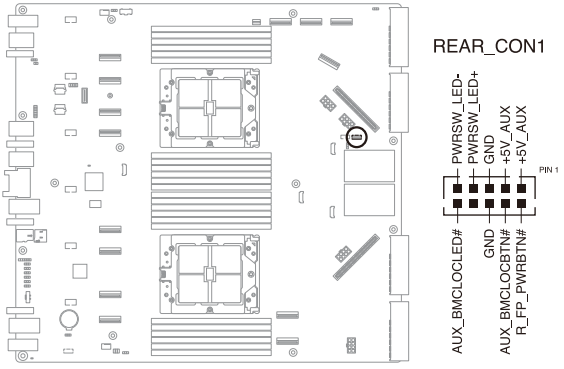
This connector is for the serial COM port. Connect the serial port module cable to one of these connectors, then install the module to a slot opening at the back of the system chassis.



K14PN-D24 COM1

7. REAR_CON1 connector (10-pin REAR_CON1)

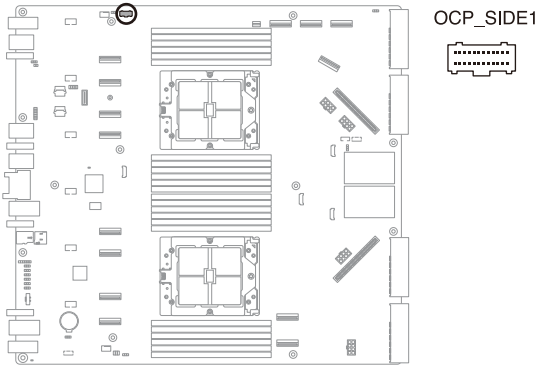
This connector is for the power and location button on the rear I/O board.



K14PN-D24 REAR_CON1

8. OCP3.0 sideband signal connector (12-pin OCP_SIDE1)

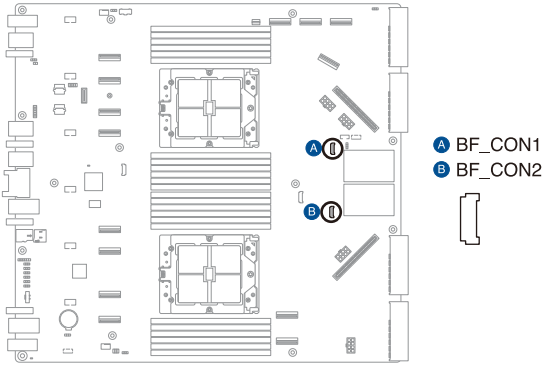
This connector is for OCP3.0 sideband signal and allows you to connect an external OCP3.0 card to support additional features.



K14PN-D24 OCP_SIDE1

9. BF connector (BF_CON1-2)

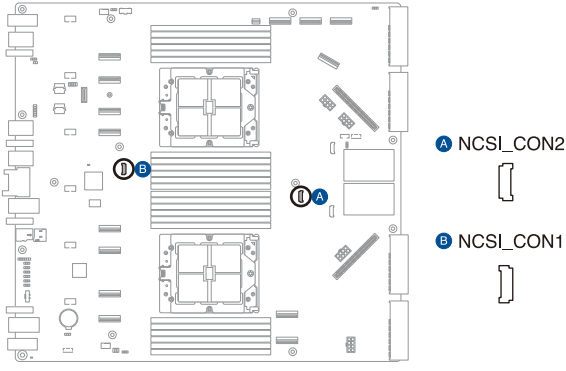
These connectors are for NCSI signals.



K14PN-D24 BF_CON

10. NCSI_CON connector (NCSI_CON1-2)

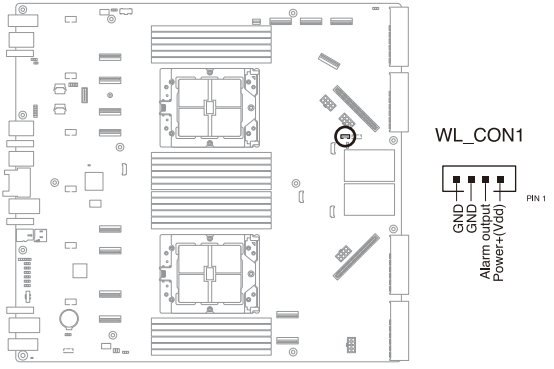
These connectors are for NCSI signals.



K14PN-D24 NCSI_CON

11. Leak detection sensor connector (4-pin WL_CON1)

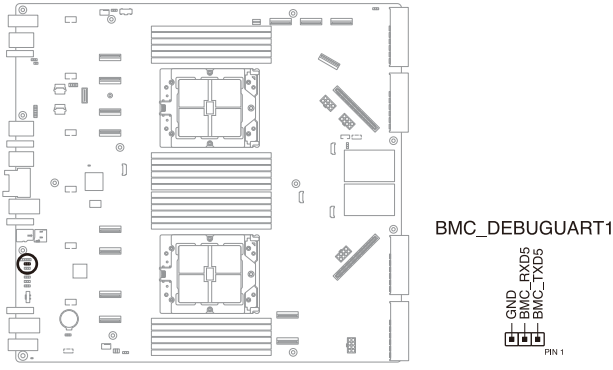
This connector allows you to connect a compatible leak detection sensor.



K14PN-D24 WL_CON1

12. BMC Debug UART connector (3-pin BMC_DEBUGUART1)

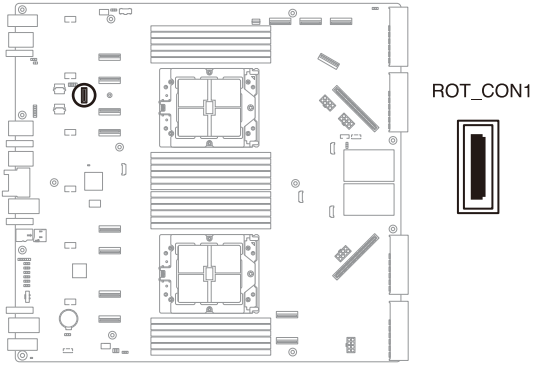
This connector is used for reading the BMC UART Debug log.



K14PN-D24 BMC_DEBUGUART1

13. Platform Firmware Resilience (PFR) Module connector (ROT_CON1)

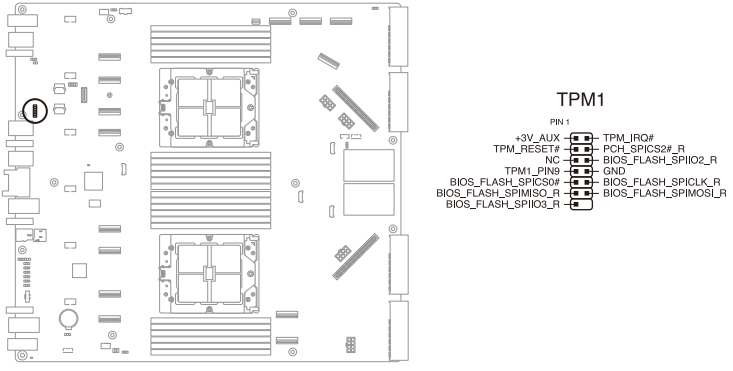
This connector allows you to connect a PFR module to enable platform firmware resilience functions.



K14PN-D24 ROT_CON1

14. TPM connector (14-1 pin TPM1)

This connector supports a Trusted Platform Module (TPM) system, which can securely store keys, digital certificates, passwords, and data.

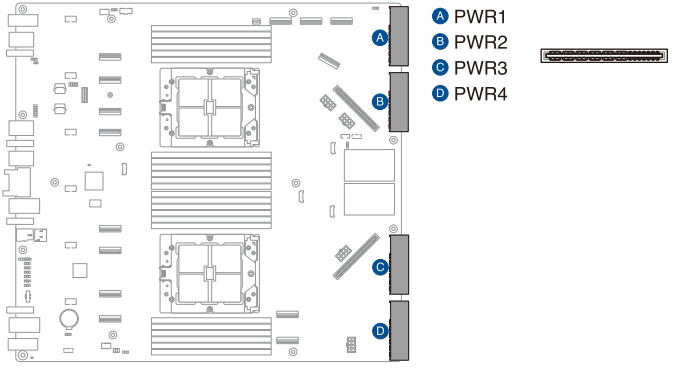


K14PN-D24 TPM1

15. System power connectors (PWR1-4)

These connectors supply power from the power supply units to the motherboard and system components.

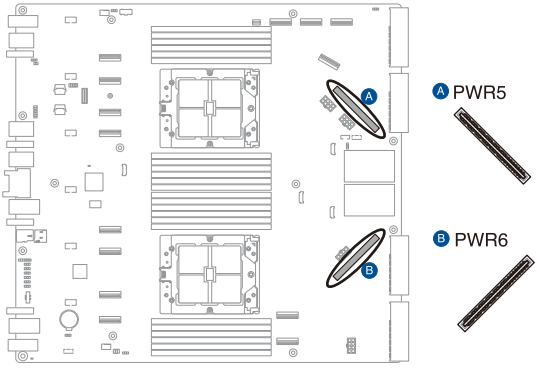
- Use of a PSU with a higher power output is recommended when configuring a system with more power-consuming devices. The system may become unstable or may not boot up if the power is inadequate.
- Ensure that your power supply unit (PSU) can provide at least the minimum power required by your system.



K14PN-D24 Power connectors

16. System power connectors (PWR5-6)

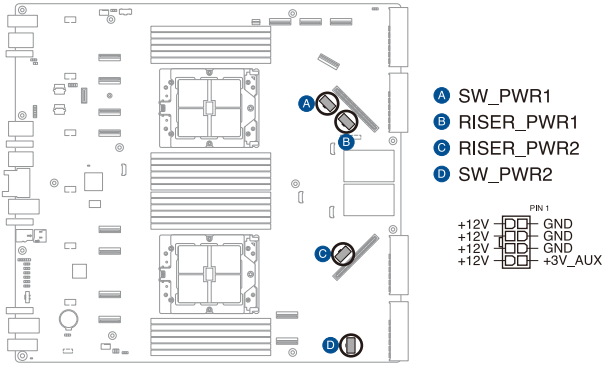
These connectors supply power from the power supply units to the motherboard and system components via the power sharing boards.



K14PN-D24 Power connectors

17. Switchboard and riser power connectors (SW_PWR1-2; RISER_PWR1-2)

These connectors supply power to the switchboard and risers.

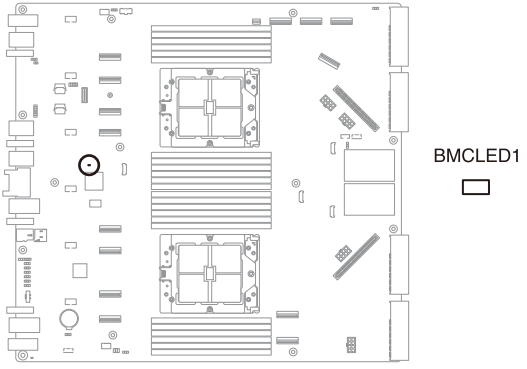


K14PN-D24 Power connectors

3.6 Onboard LEDs

1. Baseboard Management Controller LED (BMCLED1)

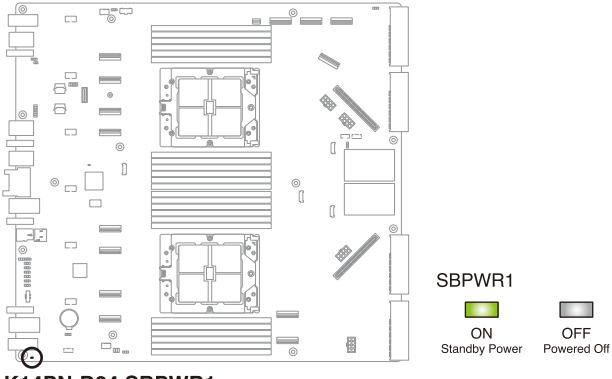
The BMC LED lights up to indicate that the on-board BMC is functional.



K14PN-D24 BMCLED1

2. Standby Power LED (SBPWR1)

The motherboard comes with a standby power LED. The green LED lights up to indicate that the system is ON, in sleep mode, or in soft-off mode. This is a reminder that you should shut down the system and unplug the power cable before removing or plugging in any motherboard component.



K14PN-D24 SBPWR1

4

BIOS Setup

This chapter tells how to change the system settings through the BIOS Setup menus. Detailed descriptions of the BIOS parameters are also provided.

4.1 Managing and updating your BIOS

The following utilities allow you to manage and update the motherboard Basic Input/Output System (BIOS) setup:

1. **ASUS CrashFree BIOS 3**

   To recover the BIOS using a bootable USB flash disk drive if the BIOS file fails or gets corrupted.

2. **ASUS EzFlash**

   Updates the BIOS using a USB flash disk.

4.1.1 ASUS CrashFree BIOS 3 utility

The ASUS CrashFree BIOS 3 is an auto recovery tool that allows you to restore the BIOS file if it fails or gets corrupted during the updating process. You can update a corrupted BIOS file using a USB flash drive that contains the updated BIOS file.

Prepare a USB flash drive containing the updated motherboard BIOS before using this utility.

Recovering the BIOS from a USB flash drive

To recover the BIOS from a USB flash drive:

1. Insert the USB flash drive with the original or updated BIOS file to one USB port on the system.
2. The utility will automatically recover the BIOS. It resets the system when the BIOS recovery finished.

DO NOT shut down or reset the system while recovering the BIOS! Doing so would cause system boot failure!

The recovered BIOS may not be the latest BIOS version for this motherboard. Visit the ASUS website at www.asus.com to download the latest BIOS file.

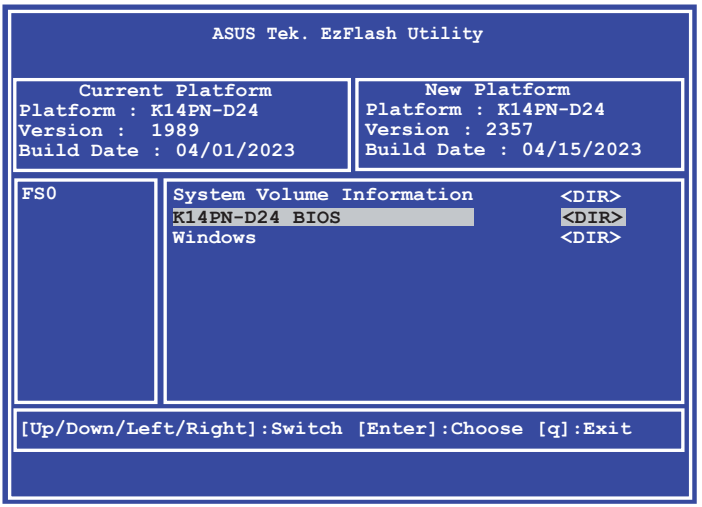
4.1.2 ASUS EZ Flash Utility

The ASUS EZ Flash Utility feature allows you to update the BIOS without having to use a DOS-based utility.

Before you start using this utility, download the latest BIOS from the ASUS website at www.asus.com.

To update the BIOS using EZ Flash Utility:

1. Insert the USB flash disk that contains the latest BIOS file into the USB port.
2. Enter the BIOS setup program. Go to the **Tool** menu, then select **Start ASUS EZ Flash**. Press <Enter>.



3. Press the Left/Right arrow keys to switch to the **Drive** field.
4. Press the Up/Down arrow keys to find the USB flash disk that contains the latest BIOS, then press <Enter>.
5. Press Left/Right arrow keys to switch to the **Folder Info** field.
6. Press the Up/Down arrow keys to find the BIOS file, then press <Enter> to perform the BIOS update process. Reboot the system when the update process is done.

- This function can support devices such as a USB flash disk with FAT 32/16 format and single partition only.
- DO NOT shut down or reset the system while updating the BIOS to prevent system boot failure!

Ensure to load the BIOS default settings to ensure system compatibility and stability. Press <F5> and select **Yes** to load the BIOS default settings.

4.2 BIOS setup program

This motherboard supports a programmable firmware chip that you can update using the provided utility described in the **Managing and updating your BIOS** section.

Use the BIOS Setup program when you are installing a motherboard, reconfiguring your system, or prompted to “Run Setup.” This section explains how to configure your system using this utility.

Even if you are not prompted to use the Setup program, you can change the configuration of your computer in the future. For example, you can enable the security password feature or change the power management settings. This requires you to reconfigure your system using the BIOS Setup program so that the computer can recognize these changes and record them in the CMOS RAM of the firmware chip.

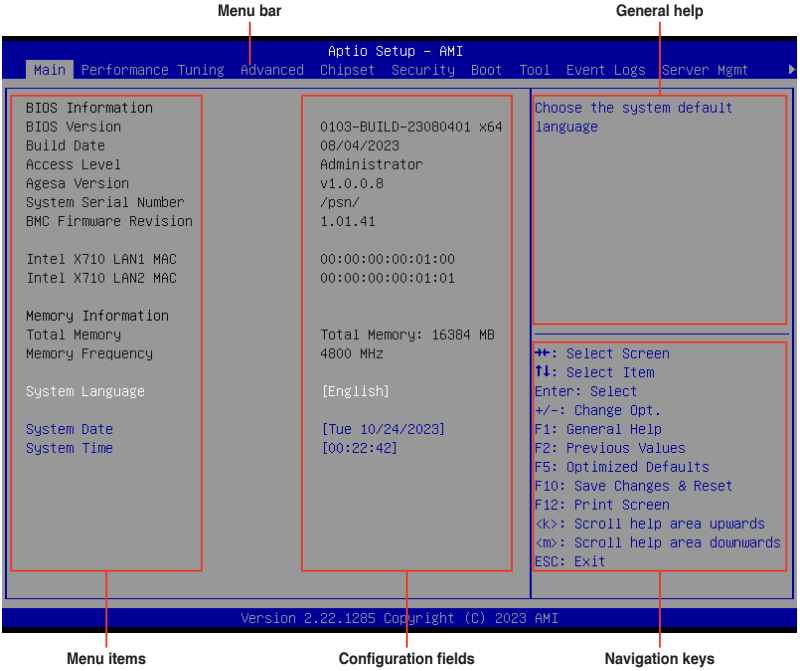
The firmware chip on the motherboard stores the Setup utility. When you start up the computer, the system provides you with the opportunity to run this program. Press <Del> during the Power-On Self-Test (POST) to enter the Setup utility; otherwise, POST continues with its test routines.

If you wish to enter Setup after POST, restart the system by pressing <Ctrl>+<Alt>+<Delete>, or by pressing the reset button on the system chassis. You can also restart by turning the system off and then back on. Do this last option only if the first two failed.

The Setup program is designed to make it as easy to use as possible. Being a menu-driven program, it lets you scroll through the various sub-menus and make your selections from the available options using the navigation keys.

- The default BIOS settings for this motherboard apply for most conditions to ensure optimum performance. If the system becomes unstable after changing any BIOS settings, load the default settings to ensure system compatibility and stability. Press <F5> and select **Yes** to load the BIOS default settings.
- Support for BIOS functions and options may vary based on AVL testing progress. Please contact your sales representative for more information.
- The BIOS setup screens shown in this section are for reference purposes only, and may not exactly match what you see on your screen.
- Visit the ASUS website (www.asus.com) to download the latest BIOS file for this motherboard.

4.2.1 BIOS menu screen



4.2.2 Menu bar

The menu bar on top of the screen has the following main items:

Main	For changing the basic system configuration
Advanced	For changing the advanced system settings
Chipset	For changing the chipset settings
Security	For changing the security settings
Boot	For changing the system boot configuration
Tool	For configuring options for special functions
Event Logs	For changing the event log settings
Server Mgmt	For changing the Server Mgmt settings
Exit	For selecting the exit options

To select an item on the menu bar, press the right or left arrow key on the keyboard until the desired item is highlighted.

Menu items

The highlighted item on the menu bar displays the specific items for that menu. For example, selecting **Main** shows the Main menu items.

The other items (such as Advanced) on the menu bar have their respective menu items.

Submenu items

A solid triangle before each item on any menu screen means that the item has a submenu. To display the submenu, select the item then press <Enter>.

Navigation keys

At the bottom right corner of a menu screen are the navigation keys for the BIOS setup program. Use the navigation keys to select items in the menu and change the settings.

General help

At the top right corner of the menu screen is a brief description of the selected item.

Configuration fields

These fields show the values for the menu items. If an item is user-configurable, you can change the value of the field opposite the item. You cannot select an item that is not user-configurable.

A configurable field is enclosed in brackets, and is highlighted when selected. To change the value of a field, select it and press <Enter> to display a list of options.

Pop-up window

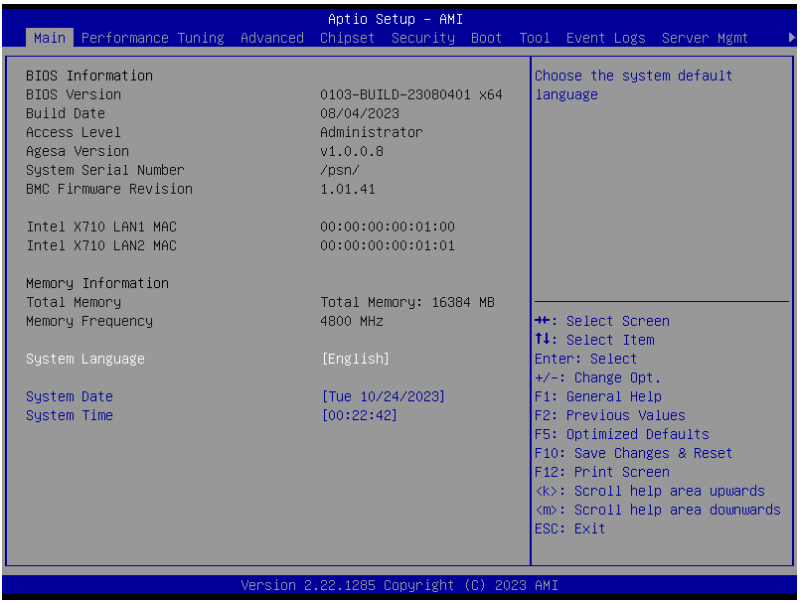
Select a menu item and press <Enter> to display a pop-up window with the configuration options for that item.

Scroll bar

A scroll bar appears on the right side of a menu screen when there are items that do not fit on the screen. Press the Up/Down arrow keys or <Page Up> / <Page Down> keys to display the other items on the screen.

4.3 Main menu

When you enter the BIOS Setup program, the Main menu screen appears. The Main menu provides you an overview of the basic system information, and allows you to set the system date, time, and language settings.



System Language

Allows you to set the system language.

System Date [MM/DD/YYYY]

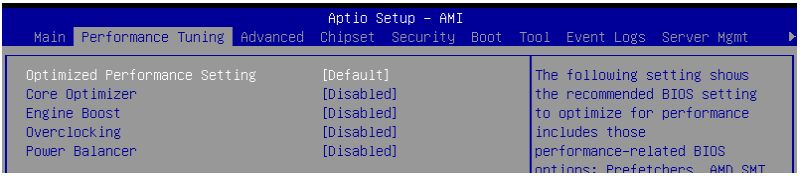
Allows you to set the system date.

System Time [HH:MM:SS]

Allows you to set the system time.

4.4 Performance Tuning menu

The Performance Tuning menu items allow you to change performance related settings for different scenarios.



Optimized Performance Setting [Default]

Allows you to select performance settings for different scenarios.

[Default] Default settings.

[By Benchmark] Optimize for different kinds of benchmarks. Select this option, then select a benchmark type from the >> list.

[By Workload] Optimize for different kinds of workloads. Select this option, then select a workload type from the >> list.

This function will reset some BIOS settings that you have changed back to their default values. Please check your BIOS settings again.

The following item appears only when **Power Balancer** is set to **[Disabled]**, or if Optimized Performance Setting is set to **[Default]** or **[By Benchmark]**.

Core Optimizer [Disabled]

Allows you to keep the processor operating at the turbo highest frequency for the maximum performance.
Configuration options: [Disabled] [Auto] [Manual]

The following item appears only when you set **Core Optimizer** to **[Manual]**.

CPU Max frequency [XXXX]
The default value for this option will be the maximum supported frequency of the CPU installed and may vary between different CPUs.

The following item appears only when **Optimized Performance Setting** is set to **[Default] or [By Benchmark]**.

Engine Boost [Disabled]

Enable this item to boost the CPU's frequency. Recommended operation at an ambient temperature of 25°C or below for optimized performance.
Configuration options: [Disabled] [Normal] [Aggressive]

Operate with an ambient temperature of 25°C or lower for optimized performance.

Overclocking [Disabled]

Enable this item to increase the CPU's clock. Please use an external PCIe storage controller for your hard drives when enabling this feature.
Configuration options: [Disabled] [Enabled]

Please note that overclocking might cause component damage or system crashes, which may reduce the lifespan of the system and the CPU. Use this tool at your own risk.

The following item appears only when **Core Optimizer** is set to **[Disabled]**, or if **Optimized Performance Setting** is set to **[Default]** or **[By Benchmark]**.

Power Balancer [Disabled]

Allows you to dynamically adjust the frequency of all CPU cores based on their current utilization, delivering better performance per watt for improved system energy efficiency.
Configuration options: [Disabled] [Enabled by ACC]

When setting **Power Balancer** to **[Enabled by ACC]**, make sure that you have the latest ASUS Control Center software installed to support Power Balancer. Please see below for recommended software versions:
- ACC: 1.4.3.5 version or above.

The following item appears only when **Power Balancer** is set to **[Enabled by ACC]**.

Policy [Auto]
Configuration options: [Auto] [Manual]

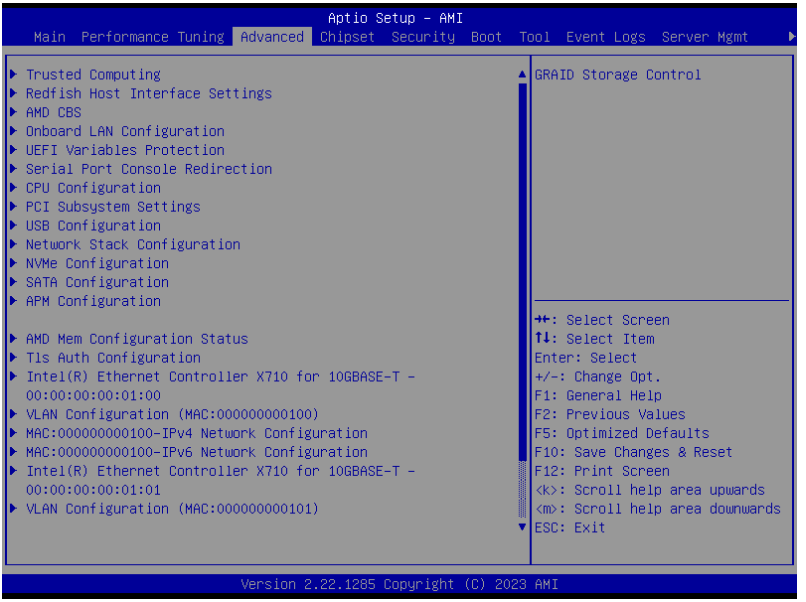
The following item appears only when set **Policy** is set to **[Manual]**.

CPU Max frequency [XXXX]
The default value for this option will be the maximum supported frequency of the CPU installed and may vary between different CPUs.

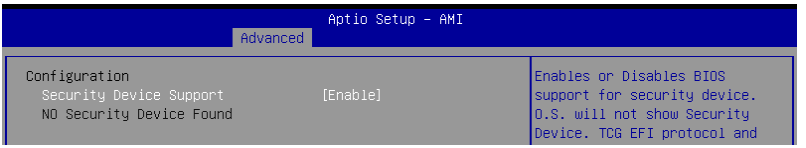
4.5 Advanced menu

The Advanced menu items allow you to change the settings for the CPU and other system devices.

Take caution when changing the settings of the Advanced menu items. Incorrect field values can cause the system to malfunction.



4.5.1 Trusted Computing

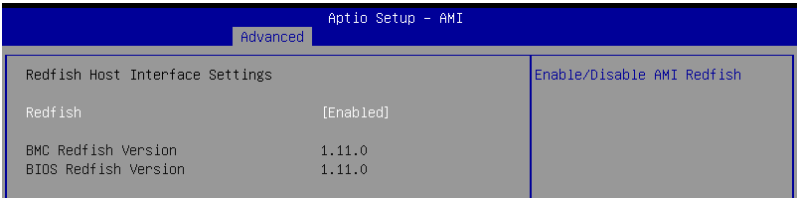


Configuration

Security Device Support [Enabled]

Configuration options: [Disabled] [Enabled]

4.5.2 Redfish Host Interface Settings



Redfish [Enabled]

Allows you to enable or disable AMI Redfish.
Configuration options: [Disabled] [Enabled]

The following items appear only when **Redfish** is set to **[Enabled]**.

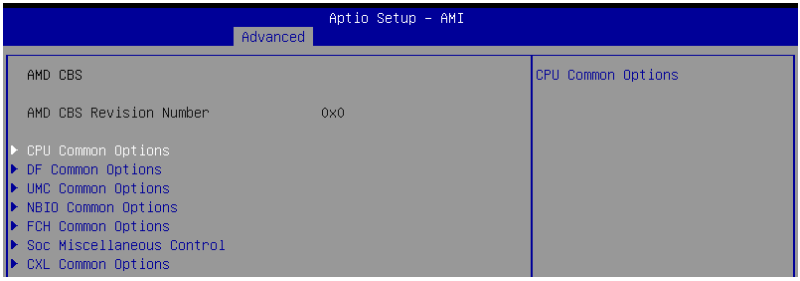
Authentication Mode [Basic Authentication]
Configuration options: [Basic Authentication] [Session Authentication]

IP Address
Allows you to set the IP address

IP Mask Address
Allows you to set the IP mask address

IP Port
Allows you to set the IP port

4.5.3 AMD CBS



CPU Common Options

Performance

Allows you to configure performance options.

REP-MOV/STOS Streaming [Enabled]

Allows you to enable or disable the use of non-caching streaming stores for large sizes.
Configuration options: [Disabled] [Enabled]

Prefetcher Settings

Allows you to configure prefetcher options.

Core Watchdog

Allows you to configure core watchdog options.

RedirectForReturnDis [Auto]

Allows you to set RedirectForReturnDis to 0, 1, or Auto as a workaround for GCC/C000005 issue for XV Core on CZ A0.
Configuration options: [Auto] [1] [0]

Platform First Error Handling [Auto]

Allows you to enable or disable PFEH, cloak individual banks, and mask deferred error interrupts from each bank.
Configuration options: [Disabled] [Enabled] [Auto]

Core Performance Boost [Auto]

Configuration options: [Disabled] [Auto]

Global C-State Control [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Power Supply Idle Control [Auto]

Configuration options: [Low Current Idle] [Typical Current Idle] [Auto]

SEV-ES ASID Space Limit [1]

Allows you to set the SEV-ES ASID Space Limit.

SEV Control [Enabled]

Configuration options: [Disabled] [Enabled]

Streaming Stores Control [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Local APIC Mode [Auto]

Configuration options: [Compatibility] [xAPIC] [x2APIC] [Auto]

ACPI _CST C1 Declaration [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

ACPI CST C2 Latency [800]

Allows you to set the ACPI CST C2 latency in microseconds.

MCA Error Threshold Enable [Auto]

Configuration options: [False] [True] [Auto]

The following item appears only when **MCA Error Threshold Enable** is set to **[True]**.

MCA Error Threshold Count [FF5]
Allows you to set the MCA error threshold count.

MCA FruText [True]

Configuration options: [False] [True]

SMU and PSP Debug Mode [Auto]

If this option is enabled, uncorrected errors detected by the PSP FW or SMU FW will hang and not reset the system instead of causing a cold reset.
Configuration options: [Disabled] [Enabled] [Auto]

PPIN Opt-in [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

SNP Memory (RMP Table) Coverage [Auto]

Configuration options: [Disabled] [Enabled] [Custom] [Auto]

The following item appears only when **SNP Memory (RMP Table) Coverage** is set to **[Enabled]** or **[Custom]**.

Split RMP Table [Auto]
Configuration options: [Disabled] [Enabled] [Auto]

The following item appears only when **SNP Memory (RMP Table) Coverage** is set to **[Custom]**.

Amount of Memory to Cover [2000]
Allows you to set the amount of system memory (MB) to be covered in hex.

SMEE [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Action on BIST Failure [Auto]

Allows you to configure what action is taken when a CCD BIST failure is detected.
Configuration options: [Do nothing] [Down-CCD] [Auto]

Fast Short REP MOVSB (FSRM) [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Enhanced Short REP MOVSB/STOSB (ESRM) [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Log Transparent Errors [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

AVX512 [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

MONITOR and MWAIT Disable [Auto]

When this option is enabled, MONITOR, MWAIT, MONITORX, and MWAITX opcodes become invalid.
Configuration options: [Disabled] [Enabled] [Auto]

Small Hammer Configuration [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Corrector Branch Predictor [Disabled]

Configuration options: [Disabled] [Enabled]

PAUSE Delay [Auto]

Configuration options: [Auto] [Disabled] [16 cycles] [32 cycles] [64 cycles] [128 cycles]

CPU Speculative Store Modes [Auto]

Configuration options: [Balanced] [More Speculative] [Less Speculative] [Auto]

Prefetch/Request Throttle [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

DF Common Options

Memory Addressing

Allows you to configure memory addressing options.

ACPI

Allows you to configure ACPI options.

Link

Allows you to configure Link options.

SDCI

Allows you to configure SDCI options.

Probe Filter

Allows you to configure Probe Filter options.

DF Watchdog Timer Interval [Auto]

Configuration options: [Auto] [41ms] [166ms] [334ms] [669ms] [1.34 seconds] [2.68 seconds] [5.36 seconds]

Disable DF to external IP Sync Flood Propagation [Auto]

Configuration options: [Sync flood disabled] [Sync flood enabled] [Auto]

Sync Flood Propagation to DF Components [Auto]

Configuration options: [Sync flood disabled] [Sync flood enabled] [Auto]

Freeze DF Module Queues on Error [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

CC6 Memory Region Encryption [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

CC6 B/W Balance Throttle Level [Auto]

Configuration options: [Auto] [Level 0] [Level 1] [Level 2] [Level 3] [Level 4]

UMC Common Options

DDR Addressing Options

Allows you to configure DDR addressing options.

DDR Controller Configuration

Allows you to configure DDR controller options.

DDR MBIST Options

Allows you to configure DDR MBIST options.

DDR RAS

Allows you to configure DDR RAS options.

DDR Bus Configuration

Allows you to configure DDR Bus options.

DDR Timing Configuration

Allows you to configure DDR Timing options.

DDR Training Options

Allows you to configure DDR Training options.

DDR Security

Allows you to configure DDR Security options.

DDR PMIC Configuration

Allows you to configure DDR PMIC options.

DDR Miscellaneous

Allows you to configure DDR Miscellaneous options.

DDR PHY (CMN)

Allows you to configure DDR PHY (CMN) options.

NBIO Common Options

IOMMU [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

DMAr Support [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

DMA Protection [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

DRTM Virtual Device Support [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

DRTM Memory Reservation [Auto]

Allows you to enable or disable reservation of 128MB memory below Bottom IO for DRTM. This option is required for Secured-Core Server functionality.
Configuration options: [Disabled] [Enabled] [Auto]

The following item appears only when **Enable AER Cap** is set to **[Enabled]** or **[Auto]**.

ACS Enable [Auto]
Configuration options: [Disabled] [Enabled] [Auto]

PCIe ARI Support [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

PCIe ARI Enumeration [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

PCIe Ten Bit Tag Support [Auto]

Allows you to enable PCIe ten bit tags for supported devices. Support is disabled if this option is enabled.
Configuration options: [Disabled] [Enabled] [Auto]

SMU Common Options

Allows you to configure SMU Common options.

NBIO RAS Common Options

Allows you to configure NBIO RAS Common options.

Enable AER Cap [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Early Link Speed [Auto]

Configuration options: [Gen1] [Gen2] [Auto]

Hot Plug Handling Mode [Auto]

Configuration options: [OS First] [Firmware First/EDR if OS supports] [Firmware First but allow OS First] [System Firmware Intermediary] [Auto]

Hot Plug Allow FF in Synchronous [Disabled]

Configuration options: [Disabled] [Enabled]

Presence Detect Select Mode [Auto]

Configuration options: [OR] [AND] [In-band only] [Out-of-band only] [Auto]

Data Link Feature Cap [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

CV Test [Auto]

Allows you to enable or disable support for PCIECV tool. Hardware defaults are preserved if this option is set to Auto.

Configuration options: [Disabled] [Enabled] [Auto]

SEV-SNP Support [Disabled]

Configuration options: [Disabled] [Enabled] [Auto]

Allow Compliance [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

SRIS [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Multi Upstream Auto Speed Change [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Multi Auto Speed Change on Last Rate [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

PCIe Link Speed Capability [Auto]

Configuration options: [Maximum speed] [Gen1] [Gen2] [Gen3] [Gen4] [Gen5] [Auto]

RTM Margining Support [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

EQ Bypass To Highest Rate [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Non-PCIe Compliant Support [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

PCIe Idle Power Setting [Optimize for Perf/Power]

Configuration options: [Optimize for Perf/Power] [Optimize for Latency]

nBif Common Options

Allows you to configure nBif Common options.

Link EQ Preset Options

Allows you to configure Link EQ Preset options.

Enable 2 SPC (Gen 4) [Auto]

Configuration options: [Enabled] [Auto]

FCH Common Options

I3C/I2C Configuration Options

Allows you to configure I3C/I2C options.

SATA Configuration Options

Allows you to configure SATA options.

USB Configuration Options

Allows you to configure USB options.

AC Power Loss Options

Allows you to configure AC power loss options.

UART Configuration Options

Allows you to configure UART options.

ESPI Configuration Options

Allows you to configure ESPI options.

FCH RAS Options

Allows you to configure FCH RAS options.

Miscellaneous Options

Allows you to configure miscellaneous FCH options.

SOC Miscellaneous Control

ABL Console Out Control [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

The following items appear only when **ABL Console Out Control** is set to **[Enabled]**.

ABL Console Out Serial Port [Auto]
Configuration options: [eSPI UART] [SOC UART0] [SOC UART1] [Auto]

The following item appears only when **ABL Console Out Serial Port** is set to **[eSPI UART]**.

ABL Console Out Serial Port IO [Auto]
Configuration options: [0x3F8] [0x2F8] [0x3E8] [0x2E8] [Auto]

ABL Basic Console Out Control [Auto]
Configuration options: [Disabled] [Enabled] [Auto]

ABL PMU Message Control [Auto]
Allows you to control the number of PMU debug messages.

Configuration options: [Detailed debug messages] [Coarse debug messages] [Stage completion] [Assertion messages] [Firmware completion messages only] [Auto]

ABL Memory Population Message Control [Warning Message]

Configuration options: [Warning Message] [Fatal Error]

PSP Error Injection Support [False]

Configuration options: [False] [True]

Firmware Anti-rollback (FAR)

Allows you to configure Firmware Anti-Rollback (FAR) options.

CXL Common Options

CXL Control [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

CXL SPM [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

CXL Encryption [Disabled]

Configuration options: [Disabled] [Enabled]

CXL DVSEC Lock [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

Temp Gen5 Advertisement [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

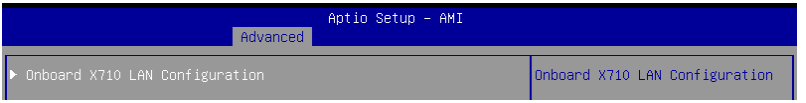
Sync Header Bypass [Auto]

Configuration options: [Disabled] [Enabled] [Auto]

CXL RAS

Allows you to configure CXL RAS options.

4.5.4 Onboard LAN Configuration



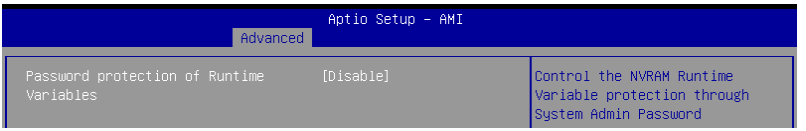
Onboard X710 LAN Configuration

LAN1/LAN2

LAN Enable [LAN1, LAN2 Enabled]

Configuration options: [Disabled] [LAN1 Enabled Only] [LAN1, LAN2 Enabled]

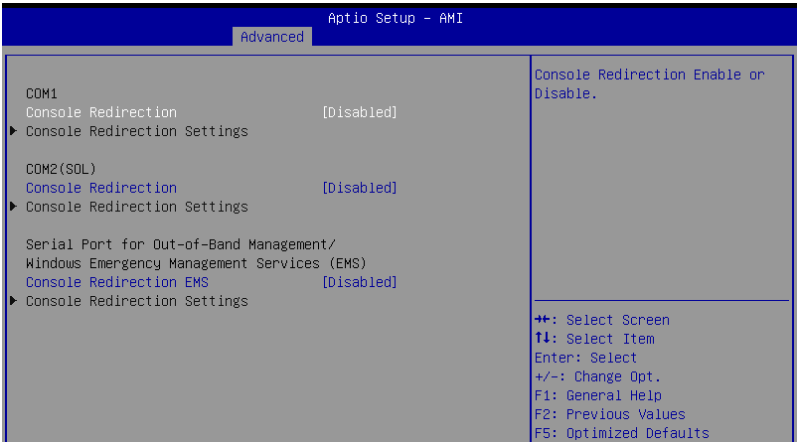
4.5.5 UEFI Variables Protection



Password protection of Runtime Variables [Disabled]

Configuration options: [Disabled] [Enabled]

4.5.6 Serial Port Console Redirection



COM1/COM2(SOL)

Console Redirection [Disabled]

Allows you to enable or disable the console redirection feature.
Configuration options: [Disabled] [Enabled]

The following item is available only when **Console Redirection** for **COM1** or **COM2(SOL)** is set to **[Enabled]**.

Console Redirection Settings

These items become configurable only when you enable the **Console Redirection** item. The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.

Terminal Type [VT100Plus]

Allows you to set the terminal type.

[VT100]	ASCII char set.
[VT100Plus]	Extends VT100 to support color, function keys, etc.
[VT-UTF8]	Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.
[ANSI]	Extended ASCII char set.

Bits per second [115200]

Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
Configuration options: [9600] [19200] [38400] [57600] [115200]

Data Bits [8]

Configuration options: [7] [8]

Parity [None]

A parity bit can be sent with the data bits to detect some transmission errors. [Mark] and [Space] parity do not allow for error detection.

[None] None

[Even] Parity bit is 0 if the number of 1's in the data bits is even.

[Odd] Parity bit is 0 if number of 1's in the data bits is odd.

[Mark] Parity bit is always 1.

[Space] Parity bit is always 0.

Stop Bits [1]

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning.) The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.
Configuration options: [1] [2]

Flow Control [None]

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
Configuration options: [None] [Hardware RTS/CTS]

VT-UTF8 Combo Key Support [Enabled]

This allows you to enable the VT -UTF8 Combination Key Support for ANSI/VT100 terminals.
Configuration options: [Disabled] [Enabled]

Recorder Mode [Disabled]

With this mode enabled only text will be sent. This is to capture Terminal data.
Configuration options: [Disabled] [Enabled]

Resolution 100x31 [Enabled]

This allows you enable or disable extended terminal solution.
Configuration options: [Disabled] [Enabled]

Putty Keypad [VT100]

This allows you to select the Function Key and Keypad on Putty.
Configuration options: [VT100] [LINUX] [XTERMR6] [SCO] [ESCN] [VT400]

Serial Port for Out-of-Band Management/ Windows Emergency Management Services (EMS)

Console Redirection EMS [Enabled]

Allows you to enable or disable the console redirection feature.
Configuration options: [Disabled] [Enabled]

The following item is available only when **Console Redirection EMS** is set to **[Enabled]**.

Console Redirection Settings

Out-of-Band Mgmt Port [COM1]

Microsoft Windows Emergency Management Services (EMS) allow for remote management of a Windows Server OS through a serial port.
Configuration options: [COM1] [COM2(SOL)]

Terminal Type EMS [VT-UTF8]

VT-UTF8 is the preferred terminal type for out0of-band management. The next best choice is VT100+, and then VT100. See above, in Console Redirection Settings page for more help with Terminal Type/Emulation.
Configuration options: [VT100] [VT100+] [VT-UTF8] [ANSI]

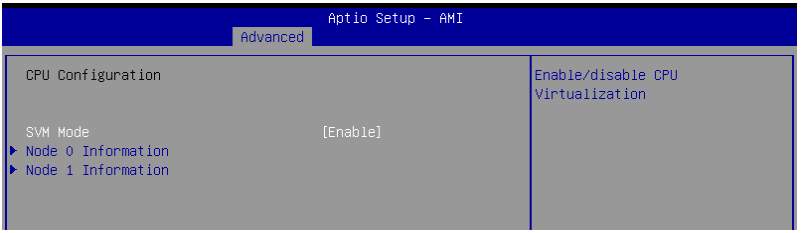
Bits per second EMS [115200]

Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
Configuration options: [9600] [19200] [57600] [115200]

Flow Control EMS [None]

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a “stop” signal can be sent to stop the data flow. Once the buffers are empty, a “start” signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
Configuration options: [None] [Hardware RTS/CTS] [Software Xon/Xoff]

4.5.7 CPU Configuration



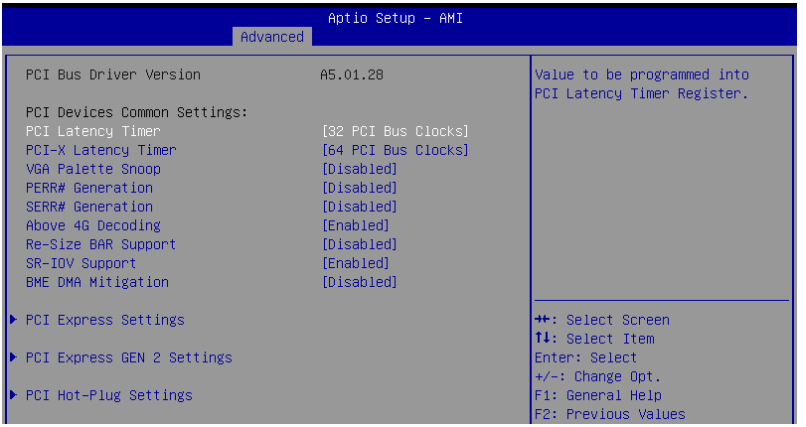
SVM Mode [Enable]

This item allows you enable or disable CPU Virtualization.
Configuration options: [Disabled] [Enable]

Node Information

This item allows you to view memory information related to the selected node.

4.5.8 PCI Subsystem Settings



PCI Latency Timer [32 PCI Bus Clocks]

Configuration options: [32 PCI Bus Clocks] [64 PCI Bus Clocks] [96 PCI Bus Clocks] [128 PCI Bus Clocks] [160 PCI Bus Clocks] [192 PCI Bus Clocks] [224 PCI Bus Clocks] [248 PCI Bus Clocks]

PCI-X Latency Timer [64 PCI Bus Clocks]

Configuration options: [32 PCI Bus Clocks] [64 PCI Bus Clocks] [96 PCI Bus Clocks] [128 PCI Bus Clocks] [160 PCI Bus Clocks] [192 PCI Bus Clocks] [224 PCI Bus Clocks] [248 PCI Bus Clocks]

VGA Palette Snoop [Disabled]

Configuration options: [Disabled] [Enabled]

PERR# Generation [Disabled]

Configuration options: [Disabled] [Enabled]

SERR# Generation [Disabled]

Configuration options: [Disabled] [Enabled]

Above 4G Decoding [Enabled]

Configuration options: [Disabled] [Enabled]

Re-Size BAR Support [Disabled]

Configuration options: [Disabled] [Enabled]

SR-IOV Support [Enabled]

Configuration options: [Disabled] [Enabled]

BME DMA Mitigation [Disabled]

Configuration options: [Disabled] [Enabled]

PCI Express Settings

Allows you to configure PCI Express options.

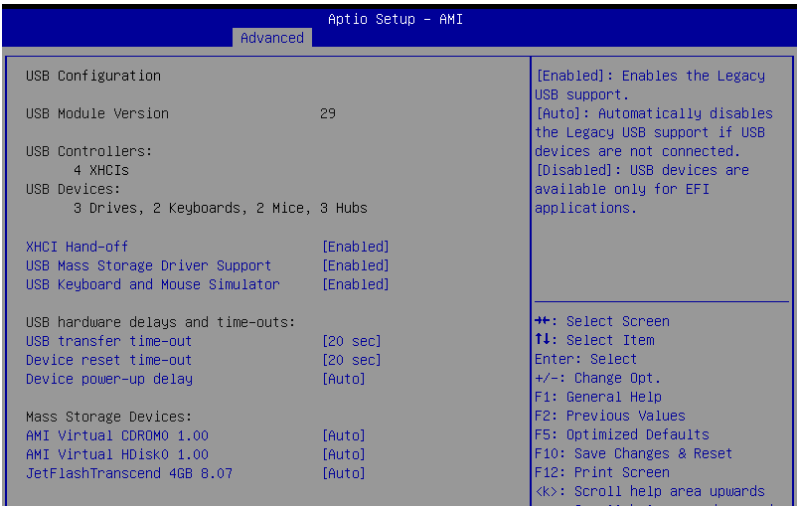
PCI Express GEN 2 Settings

Allows you to configure PCI Express GEN 2 options.

PCI Hot-Plug Settings

Allows you to configure PCI Hot-Plug options.

4.5.9 USB Configuration



XHCI Hand-off [Enabled]

Configuration options: [Enabled] [Disabled]

USB Mass Storage Driver Support [Enabled]

Configuration options: [Disabled] [Enabled]

USB Keyboard and Mouse Simulator [Enabled]

Configuration options: [Disabled] [Enabled]

USB Transfer Time-out [20 sec]

Configuration options: [1 sec] [5 sec] [10 sec] [20 sec]

Device Reset Time-out [20 sec]

Configuration options: [10 sec] [20 sec] [30 sec] [40 sec]

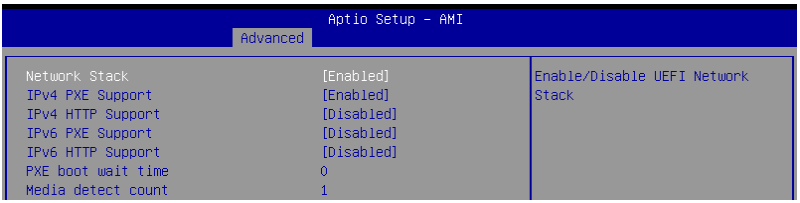
Device Power-up Delay [Auto]

Configuration options: [Auto] [Manual]

Mass Storage Devices

Allows you to select the mass storage device emulation type for devices connected.
Configuration options: [Auto] [Floppy] [Forced FDD] [Hard Disk] [CD-ROM]

4.5.10 Network Stack Configuration



Network Stack [Enabled]

Configuration options: [Disabled] [Enabled]

The following items appear only when **Network Stack** is set to **[Enabled]**.

IPv4 PXE Support [Enabled]

Configuration options: [Disabled] [Enabled]

IPv4 HTTP Support [Disabled]

Configuration options: [Disabled] [Enabled]

IPv6 PXE Support [Disabled]

Configuration options: [Disabled] [Enabled]

IPv6 HTTP Support [Disabled]

Configuration options: [Disabled] [Enabled]

PXE boot wait time [0]

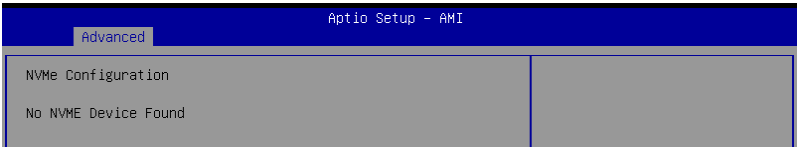
Wait time to press ESC key to abort the PXE boot.

Media detect count [1]

Wait time (in seconds) to detect media.

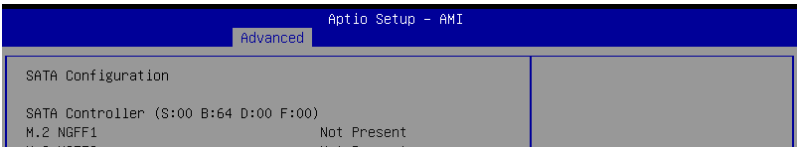
4.5.11 NVMe Configuration

This page will display the NVMe controller and drive information.



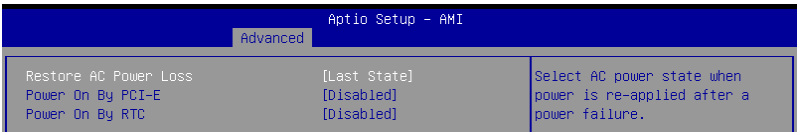
4.5.12 SATA Configuration

This page will display the SATA controller and drive information.



4.5.13 APM Configuration

Allows you to configure the Advance Power Management (APM) settings.



Restore AC Power Loss [Last State]

When set to [Power Off], the system goes into off state after an AC power loss. When set to [Power On], the system will reboot after an AC power loss. When set to [Last State], the system goes into either off or on state, whatever the system state was before the AC power loss.
Configuration options: [Power On] [Power Off] [Last State]

Power On By PCIe [Disabled]

[Disabled] Disables wake events from PCIe devices.

[Enabled] Enables wake events from PCIe devices.

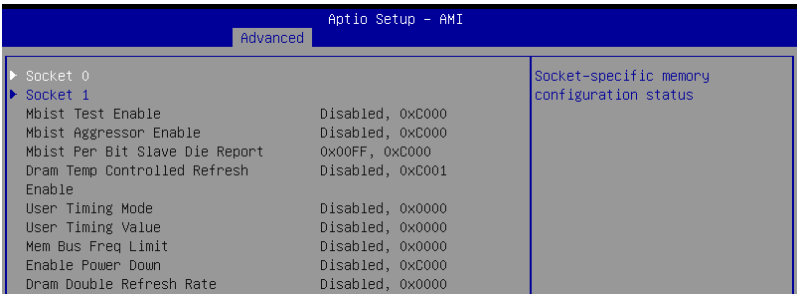
Power On By RTC [Disabled]

[Disabled] Disables RTC to generate a wake event.

[Enabled] When set to [Enabled], the items **RTC Alarm Date (Days)** and **Hour/Minute/Second** will become user-configurable with set values.

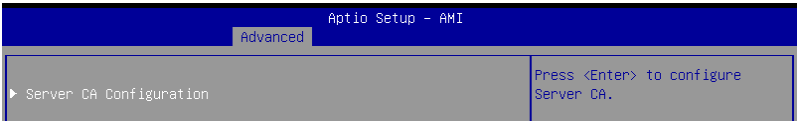
4.5.14 AMD Mem Configuration Status

The items in this menu display the memory configuration (initialized by ABL) status.



4.5.15 T1s Auth Configuration

Allows you to configure the Server Certificate Authority (CA).



Enroll Cert

Allows you to enroll a certificate using a certificate file or manually input a certificate GUID.

Enroll Cert Using File

Allows you to enroll a certificate using a certificate file. You will be prompted to select a storage device and navigate to the location of the certificate file.

Cert GUID

Allows you to enroll a certificate by manually inputting the certificate GUID.

Commit Changes and Exit

Exit Server CA configuration after saving the changes.

Discard Changes and Exit

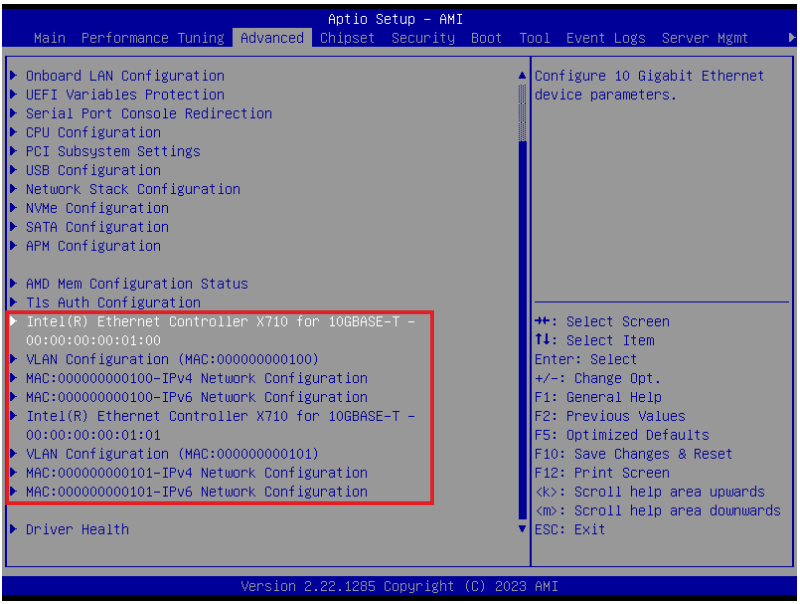
Exit Server CA configuration without saving any changes.

Delete Cert

Allows you to delete the certificate.

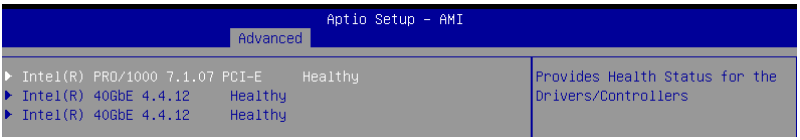
4.5.16 Third-party UEFI driver configurations

Additional configuration options for third-party UEFI drivers installed to the system will appear in the section marked in red in the screenshot below.



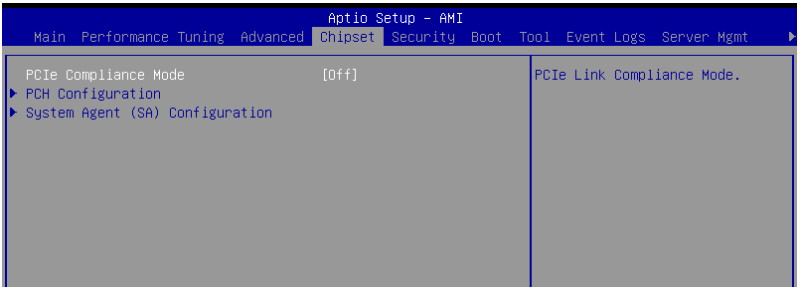
4.5.17 Driver Health

This page will display the driver and controller health status.



4.6 Chipset menu

The Chipset menu items allow you to change the Chipset settings.



PCIe Compliance Mode [Off]

This item allows you to turn the PCIe Compliance Mode on or off.
Configuration options: [Off] [On]

PCH Configuration

SB Debug Configuration

Allows you to configure SB Debug options.

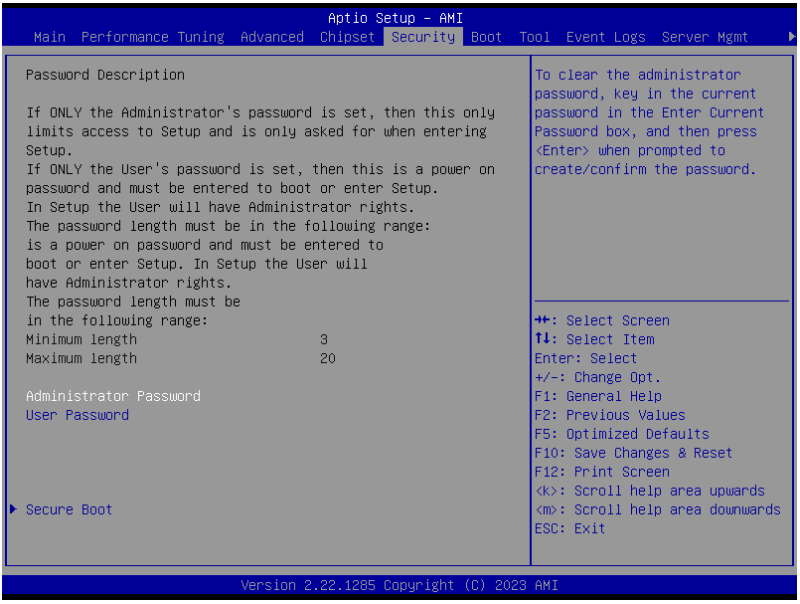
System Agent (SA) Configuration

Socket Information

This item displays the memory information for the selected socket.

4.7 Security menu

This menu allows a new password to be created or a current password to be changed. The menu also enables or disables the Secure Boot state and lets the user configure the System Mode state.



Administrator Password

To set an administrator password:

1. Select the Administrator Password item and press <Enter>.
2. From the Create New Password box, key in a password, then press <Enter>.
3. Confirm the password when prompted.

To change an administrator password:

1. Select the Administrator Password item and press <Enter>.
2. From the Enter Current Password box, key in the current password, then press <Enter>.
3. From the Create New Password box, key in a new password, then press <Enter>.
4. Confirm the password when prompted.

To clear the administrator password, follow the same steps as in changing an administrator password, but press <Enter> when prompted to create/confirm the password.

User Password

To set a user password:

1. Select the User Password item and press <Enter>.
2. From the Create New Password box, key in a password, then press <Enter>.
3. Confirm the password when prompted.

To change a user password:

1. Select the User Password item and press <Enter>.
2. From the Enter Current Password box, key in the current password, then press <Enter>.
3. From the Create New Password box, key in a new password, then press <Enter>.
4. Confirm the password when prompted.

To clear the user password, follow the same steps as in changing a user password, but press <Enter> when prompted to create/confirm the password.

Secure Boot

Secure Boot [Disabled]

Secure Boot can be enabled if the system is running in User mode with enrolled platform Key (EPK) or if the CSM function is disabled.
Configuration options: [Disabled] [Enabled]

Secure Boot Mode [Custom]

Allows you to set the Secure Boot selector.
Configuration options: [Standard] [Custom]

Install Default Secure Boot Keys

Allows you to load the default secure boot keys.

Clear Secure Boot Keys

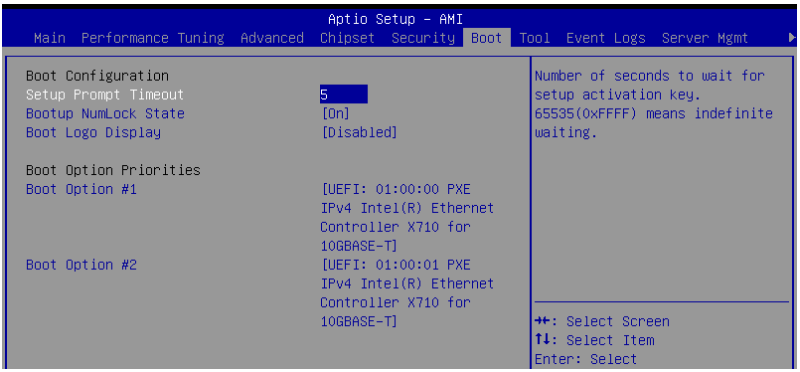
Allows you to delete all previously applied secure boot keys.

Key Management

Allows you to configure Key Management options.

4.8 Boot menu

The Boot menu items allow you to change the system boot options.



Setup Prompt Timeout [5]

Allows you to set the number of seconds that the firmware waits before initiating the original default boot selection. 65535(OxFFFF) means indefinite waiting. Use the <+> or <-> to adjust the value.

Bootup NumLock State [On]

Allows you to select the power-on state for the NumLock.
Configuration options: [On] [Off]

Boot Logo Display [Disabled]

Allows you to enable or disable Quiet Boot option.
Configuration options: [Disabled] [Enabled]

Boot Option Priorities

These items specify the boot device priority sequence from the available devices. The number of device items that appears on the screen depends on the number of devices installed in the system.

- To select the boot device during system startup, press <F8> when ASUS Logo appears.
- To access Windows OS in Safe Mode, please press <F8> after POST.

POST Report [5 sec]

Allows you to set the desired POST Report waiting time from 1 to 10 seconds.
Configuration options: [1 sec] - [10 sec] [Until Press ESC]

Hard Drive BBS Priorities

These items appear only when you connect a network cable or SATA ODD to the SATA port, and allows you to set the booting order of the Network / SATA devices.

4.9 Tool menu

The Tool menu items allow you to configure options for special functions. Select an item and press <Enter> to display the submenu.

Start ASUS EzFlash

Allows you to run ASUS EZ Flash BIOS ROM Utility. Refer to the ASUS EZ Flash Utility section for details.

IPMI Hardware Monitor

Allows you to run the IPMI hardware monitor.

ASUS SMBIOS Viewer

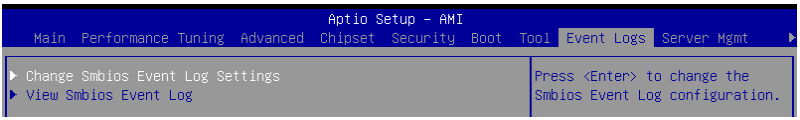
Allows you to run ASUS SMBIOS Viewer.

ASUS Storage Viewer

Allows you to run ASUS Storage Viewer.

4.10 Event Logs menu

The Event Logs menu items allow you to change the event log settings and view the system event logs.



4.10.1 Change Smbios Event Log Settings

Press <Enter> to change the Smbios Event Log configuration.

All values changed here do not take effect until computer is restarted.

Smbios Event Log [Enabled]

Change this to enable or disable all features of Smbios Event Logging during boot.
Configuration options: [Disabled] [Enabled]

The following item appears only when **Smbios Event Log** is set to **[Enabled]**.

Erase Event Log [No]

Choose options for erasing Smbios Event Log. Erasing is done prior to any logging activation during reset.
Configuration options: [No] [Yes, Next reset] [Yes, Every reset]

When Log is Full [Do Nothing]

Choose options for reactions to a full Smbios Event Log.
Configuration options: [Do Nothing] [Erase Immediately]

Log EFI Status Code [Enabled]

This option allows you to enable or disable logging of the EFI Status Codes.
Configuration options: [Disabled] [Enabled]

The following item appears only when **Log EFI Status Code** is set to **[Enabled]**.

Convert EFI Status Codes to Standard Smbios Type [Disabled]
This option allows you to enable or disable converting of EFI Status Codes to Standard Smbios Type (Not all may be translated).
Configuration options: [Disabled] [Enabled]

4.10.2 View Smbios Event Log

Press <Enter> to view all smbios event logs.

4.11 Server Mgmt menu

The Server Management menu displays the server management status and allows you to change the settings.

OS Watchdog Timer [Disabled]

This item allows you to start a BIOS timer which can only be shut off by management software after the OS loads.
Configuration options: [Enabled] [Disabled]

The following items appear only when **OS Watchdog Timer** is set to **[Enabled]**.

OS Wtd Timer Timeout [10]

Enter the value between 1 to 30 minutes to configure the length fo the OS Boot Watchdog Timer.

OS Wtd Timer Policy [Reset]

This item allows you to configure the how the system should respond if the OS Boot Watch Timer expires.
Configuration options: [Do Nothing] [Reset] [Power Down] [Power Cycle]

4.11.1 System Event Log

Allows you to change the SEL event log configuration.

Erase SEL [No]

Allows you to choose options for erasing SEL.
Configuration options: [No] [Yes, On next reset] [Yes, On every reset]

4.11.2 BMC network configuration

The sub-items in this configuration allow you to configure the BMC network parameters.

DM_LAN/Shared LAN

Configuration Address source [Previous State]

Configuration options: [Previous State] [Static] [DynamicBmcDhcp]

IPv6 Support [Enabled]

Configuration options: [Enabled] [Disabled]

The following items appear only when **IPv6 Support** is set to **[Enabled]**.

Configuration Address source [Previous State]

Configuration options: [Previous State] [Static] [DynamicBmcDhcp]

Configuration Router Lan1/Lan2 Address source [Previous State]

Configuration options: [Previous State] [Static] [DynamicBmcDhcp]

VLAN Support [Previous State]

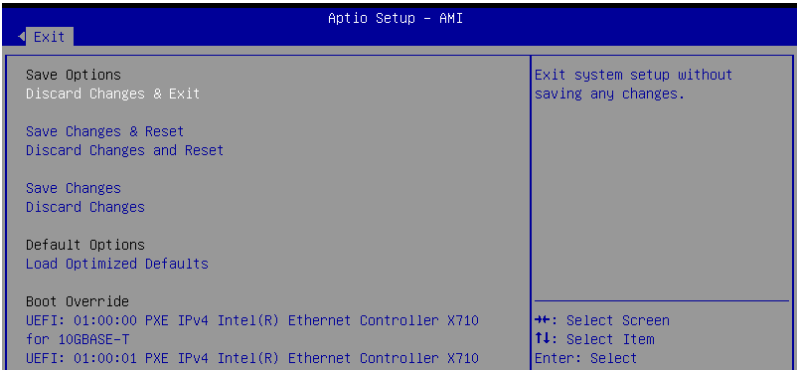
Configuration options: [Previous State] [Enabled] [Disabled]

4.11.3 View System Event Log

This item allows you to view the system event log records.

4.12 Exit menu

The Exit menu items allow you to save or discard your changes to the BIOS items.



Discard Changes and Exit

Exit system setup without saving any changes.

Save Changes and Reset

Reset the system after saving the changes.

Discard Changes and Reset

Reset system setup without saving any changes.

Save Changes

Save changes done so far to any of the setup options.

Discard Changes

Discard changes done so far to any of the setup options.

Load Optimized Defaults

Restore/load default values for all the setup options.

Boot Override

These items displays the available devices. The device items that appears on the screen depends on the number of devices installed in the system. Click an item to start booting from the selected device.

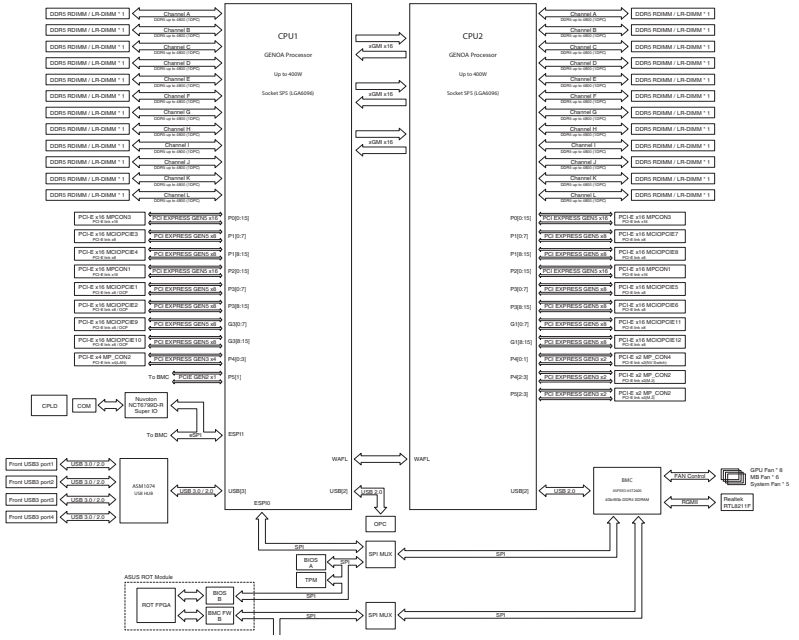
Launch EFI Shell from filesystem device

This item allows you to attempt to launch the EFI Shell application (shellx64.efi) from one of the available filesystem devices.

Appendix

This appendix includes additional information that you may refer to when configuring the motherboard.

Block diagram



Q-Code table

ACTION	PHASE	POST CODE	TYPE	DESCRIPTION
SEC Start up	Security Phase	0x01	Progress	First post code
		0x02	Progress	Load BSP microcode
		0x03	Progress	Perform early platform Initialization
		0x04	Progress	Set cache as ram for PEI phase
		0x05	Progress	Establish Stack
		0x06	Progress	CPU Early Initialization
PSP Boot	PSP Boot Loader phase (Error Post Codes)	0x00	error	General - Success
		0x01	error	Generic Error Code
		0x02	error	Generic Memory Error
		0x03	error	Buffer Overflow
		0x04	error	Invalid Parameter(s)
		0x05	error	Invalid Data Length
		0x06	error	Data Alignment Error
		0x07	error	Null Pointer Error
		0x08	error	Unsupported Function
		0x09	error	Invalid Service ID
		0x0A	error	Invalid Address
		0x0B	error	Out of Resource Error
		0x0C	error	Timeout
		0x0D	error	Data abort exception
		0x0E	error	Prefetch abort exception
		0x0F	error	Out of Boundary Condition Reached
		0x10	error	Data corruption
		0x11	error	Invalid command
		0x12	error	The package type provided by BR is incorrect
		0x13	error	Failed to retrieve FW header during FW validation
		0x14	error	Key size not supported
		0x15	error	Agesa0 verification error
		0x16	error	SMU FW verification error
		0x17	error	OEM SINGING KEY verification error
		0x18	error	Generic FW Validation error
		0x19	error	RSA operation fail - bootloader
		0x1A	error	CCP Passthrough operation failed - internal status
		0x1B	error	AES operation fail
		0x1C	error	CCP state save failed
		0x1D	error	CCP state restore failed
		0x1E	error	SHA256/384 operation fail - internal status
		0x1F	error	ZLib Decompression operation fail
		0x20	error	HMAC-SHA256/384 operation fail - internal status
		0x21	error	Booted from boot source not recognized by PSP
		0x22	error	PSP directory entry not found
		0x23	error	PSP failed to set the write enable latch
		0x24	error	PSP timed out because spirom took too long
		0x25	error	Cannot find BIOS directory
		0x26	error	SpiRom is not valid
		0x27	error	Slave die has different security state from master
		0x28	error	SMI interface init failure
		0x29	error	SMI interface generic error
		0x2A	error	Invalid die ID executes MCM related function
		0x2B	error	Invalid MCM configuration table read from bootrom
		0x2C	error	Valid boot mode wasn't detected
		0x2D	error	NVStorage init failure
		0x2E	error	NVStorage generic error
		0x2F	error	MCM 'error' to indicate slave has more data to send
		0x30	error	MCM error if data size exceeds 32B
		0x31	error	Invalid client id for SVC MCM call
		0x32	error	MCM slave status register contains bad bits
		0x33	error	MCM call was made in a single die environment
		0x34	error	PSP secure mapped to invalid segment (should be 0x400_0000)
		0x35	error	No physical x86 cores were found on die
		0x36	error	Insufficient space for secure OS (range of free SRAM to SVC stack base)
		0x37	error	SYSHUB mapping memory target type is not supported
		0x38	error	Attempt to unmap permanently mapped TLB to PSP secure region
		0x39	error	Unable to map an SMN address to AXI space
		0x3A	error	Unable to map a SYSHUB address to AXI space

(continued on the next page)

ACTION	PHASE	POST CODE	TYPE	DESCRIPTION
PSP Boot	PSP Boot Loader phase (Error Post Codes)	0x3B	error	The count of CCXs or cores provided by bootrom is not consistent
		0x3C	error	Uncompressed image size doesn't match value in compressed header
		0x3D	error	Compressed option used in case where not supported
		0x3E	error	Fuse info on all dies don't match
		0x3F	error	PSP sent message to SMU; SMU reported an error
		0x40	error	Function RunPostX86ReleaseUnitTests failed in memcmp()
		0x41	error	Interface between PSP to SMU not available.
		0x42	error	Timer wait parameter too large
		0x43	error	Test harness module reported an error
		0x44	error	x86 wrote C2PMSG_0 interrupting PSP, but the command has an invalid format
		0x45	error	Failed to read from SPI the Bios Directory or Bios Combo Directory
		0x46	error	Failed to find FW entry in SPL Table
		0x47	error	Failed to read the combo bios header
		0x48	error	SPL version mismatch
		0x49	error	Error in Validate and Loading AGESA APOB SVC call
		0x4A	error	Correct fuse bits for DIAG_BL loading not set
		0x4B	error	The UmcProgramKeys() function was not called by AGESA
		0x4C	error	Unconditional Unlock based on serial numbers failure
		0x4D	error	Syshub register programming mismatch during readback
		0x4E	error	Family ID in MP0_SFUSE_SEC[7:3] not correct
		0x4F	error	An operation was invoked that can only be performed by the GM
		0x50	error	Failed to acquire host controller semaphore to claim ownership of SMB
		0x51	error	Timed out waiting for host to complete pending transactions
		0x52	error	Timed out waiting for slave to complete pending transactions
		0x53	error	Unable to kill current transaction on host, to force idle
		0x54	error	One of: Illegal command, Unclaimed cycle, or Host time out
		0x55	error	An smbus transaction collision detected, operation restarted
		0x56	error	Transaction failed to be started or processed by host, or not completed
		0x57	error	An unsolicited smbus interrupt was received
		0x58	error	An attempt to send an unsupported PSP-SMU message was made
		0x59	error	An error/data corruption detected on response from SMU for sent msg
		0x5A	error	MCM Steady-state unit test failed
		0x5B	error	S3 Enter failed
		0x5C	error	AGESA BL did not set PSP SMU reserved addresses via SVC call
		0x5D	error	Reserved PSP/SMU memory region is invalid
		0x5E	error	CcxSecBisiEn not set in fuse RAM
		0x5F	error	Received an unexpected result
		0x60	error	VMG Storage Init failed
		0x61	error	Failure in mbedTLS user app
		0x62	error	An error occured whilst attempting to SMN map a fuse register
		0x63	error	Fuse burn sequence/operation failed due to internal SOC error
		0x64	error	Fuse sense operation timed out
		0x65	error	Fuse burn sequence/operation timed out waiting for burn done
		0x66	error	The PMU FW Public key certificate loading or authentication fails
		0x67	error	This PSP FW was revoked
		0x68	error	The platform model/vendor id fuse is not matching the BIOS public key token
		0x69	error	The BIOS OEM public key of the BIOS was revoked for this platform
		0x6A	error	PSP level 2 directory not match expected value.
		0x6B	error	BIOS level 2 directory not match expected value.
		0x6C	error	Reset image not found
		0x6D	error	Generic error indicating the CCP HAL initialization failed
		0x6E	error	Failure to copy NVRAM to DRAM.
		0x6F	error	Invalid key usage flag
		0x70	error	Unexpected fuse set
		0x71	error	RSMU signaled a security violation
		0x72	error	Error programming the WAFL PCS registers
		0x73	error	Error setting wafl PCS threshold value
		0x74	error	Error loading OEM trustlets
		0x75	error	Recovery mode accross all dies is not sync'd
		0x76	error	Uncorrectable WAFL error detected
		0x77	error	Fatal MP1 error detected
		0x78	error	Bootloader failed to find OEM signature
		0x79	error	Error copying BIOS to DRAM
		0x7A	error	Error validating BIOS image signature
		0x7B	error	OEM Key validation failed
		0x7C	error	Platform Vendor ID and/or Model ID binding violation

(continued on the next page)

ACTION	PHASE	POST CODE	TYPE	DESCRIPTION
PSP Boot	PSP Boot Loader phase (Status Post Codes)	0x7D	error	Bootloader detects BIOS request boot from SPI-ROM, which is unsupported for PSB.
		0x7E	error	Requested fuse is already blown, reblow will cause ASIC malfunction
		0x7F	error	Error with actual fusing operation
		0x80	error	(Local Master PSP on P1 socket) Error reading fuse info
		0x81	error	(Local Master PSP on P1 socket) Platform Vendor ID and/or Model ID binding violation
		0x82	error	(Local Master PSP on P1 socket) Requested fuse is already blown, reblow will cause ASIC malfunction
		0x83	error	(Local Master PSP on P1 socket) Error with actual fusing operation
		0x84	error	SEV FW Rollback attempt is detected
		0x85	error	SEV download FW command fail to broadcase and clear the IsInSRAM field on slave dies
		0x86	error	Agesa error injection failure
		0x87	error	Uncorrectable TWIX error detected
		0x88	error	Error programming the TWIX PCS registers
		0x89	error	Error setting TWIX PCS threshold value
		0x8A	error	SW CCP queue is full, cannot add more entries
		0x8B	error	CCP command description syntax error detected from input
		0x8C	error	Return value stating that the command has not yet be scheduled
		0x8D	error	The command is scheduled and being worked on
		0x8E	error	The DXIO PHY SRAM Public key certificate loading or authentication fails
		0x8F	error	fTPM binary size exceeds limit allocated in Private DRAM, need to increase the limit
		0x90	error	The TWIX link for a particular CCD is not trained Fatal error
		0x91	error	Security check failed (not all dies are in same security state)
		0x92	error	FW type mismatch between the requested FW type and the FW type embedded in the FW binary header
		0x93	error	SVC call input parameter address violation
		0x94	error	Firmware Compatibility Level mismatch
		0x95	error	Bad status returned by I2CKnollCheck
		0x96	error	NACK to general call (no device on Knoll I2C bus)
		0x97	error	Null pointer passed to I2CKnollCheck
		0x98	error	Invalid device-ID found during Knoll authentication
		0x99	error	Error during Knoll/Prom key derivation
		0x9A	error	Null pointer passed to Crypto function
		0x9B	error	Error in checksum from wrapped Knoll/Prom keys
		0x9C	error	Knoll returned an invalid response to a command
		0x9D	error	Bootloader failed in Knoll Send Command function
		0x9E	error	No Knoll device found by verifying MAC
		0x9F	error	The maximum allowable error post code
		0xA0	error	Bootloader successfully entered C Main
		0xA1	error	Master initialized C2P / slave waited for master to init C2P
		0xA2	error	HMAC key successfully derived
		0xA3	error	Master got Boot Mode and sent boot mode to all slaves
		0xA4	error	SpiRom successfully initialized
		0xA5	error	BIOS Directory successfully read from SPI to SRAM
		0xA6	error	Early unlock check
		0xA7	error	Inline Aes key successfully derived
		0xA8	error	Inline-AES key programming is done
		0xA9	error	Inline-AES key wrapper derivation is done
		0xAA	error	Bootloader successfully loaded HW IP configuration values
		0xAB	error	Bootloader successfully programmed MBAT table
		0xAC	error	Bootloader successfully loaded SMU FW
		0xAD	error	Progress code is available
		0xAE	error	User mode test Uapp completed successfully
		0xAF	error	Bootloader loaded Agesa0 from SpiRom
		0xB0	error	AGESA phase has completed
		0xB1	error	RunPostDramTrainingTests() completed successfully
		0xB2	error	SMU FW Successfully loaded to SMU Secure DRAM
		0xB3	error	Sent all required boot time messages to SMU
		0xB4	error	Validated and ran Security Gasket binary
		0xB5	error	UMC Keys generated and programmed
		0xB6	error	Inline AES key wrapper stored in DRAM
		0xB7	error	Completed FW Validation step
		0xB8	error	Completed FW Validation step
		0xB9	error	BIOS copy from SPI to DRAM complete
		0xBA	error	Completed FW Validation step

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ACTION	PHASE	POST CODE	TYPE	DESCRIPTION
PSP Boot	PSP Boot Loader phase (Status Post Codes)	0xBB	error	BIOS load process fully complete
		0xBC	error	Bootloader successfully release x86
		0xBD	error	Early Secure Debug completed
		0xBE	error	GetFWVersion command received from BIOS is completed
		0xBF	error	SMIInfo command received from BIOS is completed
		0xC0	error	Successfully entered WarmBootResume()
		0xC1	error	Successfully copied SecureOS image to SRAM
		0xC2	error	Successfully copied trustlets to PSP Secure Memory
		0xC3	error	About to jump to Secure OS (SBL about to copy and jump)
		0xC4	error	Successfully restored CCP and UMC state on S3 resume
		0xC5	error	PSP SRAM HMAC validated by Mini BL
		0xC6	error	About to jump to <t-base in Mini BL
		0xC7	error	VMG ECDH unit test started
		0xC8	error	VMG ECDH unit test passed
		0xC9	error	VMG ECC CDH primitive unit test started
		0xCA	error	VMG ECC CDH primitive unit test passed
		0xCB	error	VMG SP800-108 KDF-CTR HMAC unit test started
		0xCC	error	VMG SP800-108 KDF-CTR HMAC unit test passed
		0xCD	error	VMG LAUNCH_* test started
		0xCE	error	VMG LAUNCH_* test passed
		0xCF	error	MP1 has been taken out of reset, and executing SMUFW
		0xD0	error	PSP and SMU Reserved Addresses correct
		0xD1	error	Reached Naples steady-state WFI loop
		0xD2	error	Knoll device successfully initialized
		0xD3	error	32-byte RandOut successfully returned from Knoll
		0xD4	error	32-byte MAC successfully received from Knoll.
		0xD5	error	Knoll device verified successfully
		0xD6	error	CNLI Keys generated and programmed
		0xD7	error	Enter recovery mode due to trustlet validation fail.
		0xD8	error	Enter recovery mode due to OS validation fail.
		0xD9	error	Enter recovery mode due to OEM public key not found.
		0xDA	error	Enter recovery mode with header corruption
		0xDB	error	We should not treat this error as blocking
		0xDC	error	When same fw image type is already loaded in SRAM
		0xDD	error	0xE2 progress codes are available
		0xE0	error	Unlock return
		0xE2	error	Token expiration reset triggered
		0xE3	error	Completed DXIO PHY SRAM FW key Validation step
		0xE4	error	MP1 firmware load to SRAM success
		0xE5	error	Bootloader read the MP1 SRAM successfully
		0xE6	error	Bootloader successfully reset MP1
		0xE7	error	DF init successfully done (in absence of AGESA)
		0xE8	error	UMC init successfully done (in absence of AGESA)
		0xE9	error	LX6 Boot ROM code ready
		0xEA	error	Bootloader successfully asserted LX6 reset
		0xEB	error	LX6 load to SRAM success
		0xEC	error	Bootloader successfully set LX6 reset vector to SRAM
		0xED	error	Bootloader successfully de-asserted LX6 reset
		0xEE	error	LX6 firmware is running and ready
		0xEF	error	Loading of S3 image done successfully
		0xF0	error	Bootloader successfully verify signed image using 4K/2K key
		0xF1	error	Bootloader identified as running on SP32P or multi-socket boot
		0xF2	error	Security Policy check successful (only in secure boot)
		0xF3	error	Bootloader successfully loaded SS3
		0xF4	error	Bootloader successfully load fTPM Driver
		0xF5	error	Bootloader successfully loaded sys_drv
		0xF6	error	Bootloader successfully loaded secure OS
		0xF7	error	Bootloader about to transfer control to secureOS
		0xFF	error	Bootloader sequence finished
Quick VGA	PEI(Pre-EFI Initialization) phase	0x10	Progress	PEI Core Entry
		0x11	Progress	PEI cache as ram CPU initial
		0x15	Progress	NB Initialization before installed memory
		0x19	Progress	SB Initialization before installed memory

(continued on the next page)

ACTION	PHASE	POST CODE	TYPE	DESCRIPTION
Quick VGA	DXE(Driver Execution Environment) phase	0x32	Progress	CPU POST-Memory Initialization
		0x33	Progress	CPU Cache Initialization
		0x34	Progress	Application Processor(s) (AP) Initialization
		0x35	Progress	BSP Selection
		0x36	Progress	CPU Initialization
		0x37	Progress	Pre-memory NB Initialization
		0x3B	Progress	Pre-memory SB Initialization
		0x4F	Progress	DXE Initial Program Load(IPL)
		0x60	Progress	DXE Core Started
		0x61	Progress	DXE NVRAM Initialization
		0x62	Progress	SB run-time Initialization
		0x63	Progress	CPU DXE Initialization
		0x68	Progress	PCI HB Initialization
		0x69	Progress	NB DXE Initialization
		0x6A	Progress	NB DXE SMM Initialization
		0x70	Progress	SB DXE Initialization
		0x71	Progress	SB DXE SMM Initialization
		0x72	Progress	SB DEVICES Initialization
		0x78	Progress	ACPI Module Initialization
		0xD0	Progress	CPU PM Structure Initialization
Normal boot	BDS(Boot Device Selection) phase	0x90	Progress	BDS started
		0x91	Progress	Connect device event
		0x92	Progress	PCI Bus Enumeration
		0x93	Progress	PCI Bus Enumeration
		0x94	Progress	PCI Bus Enumeration
		0x95	Progress	PCI Bus Enumeration
		0x96	Progress	PCI Bus Enumeration
		0x97	Progress	Console output connect event
		0x98	Progress	Console input connect event
		0x99	Progress	AMI Super IO start
		0x9A	Progress	AMI USB Driver Initialization
		0x9B	Progress	AMI USB Driver Initialization
		0x9C	Progress	AMI USB Driver Initialization
		0x9D	Progress	AMI USB Driver Initialization
		0xb3	Progress	Reset system
		0xb4	Progress	USB hotplug
		0xb6	Progress	NVRAM clean up
		0xb7	Progress	NVRAM configuration reset
		0xA0	Progress	IDE, AHCI Initialization
		0xA1	Progress	IDE, AHCI Initialization
		0xA2	Progress	IDE, AHCI Initialization
		0xA3	Progress	IDE, AHCI Initialization
		0x00~0xFF	Progress	Wait BMC ready
		0xA8	Progress	BIOS Setup Utility password verify
		0xA9	Progress	BIOS Setup Utility start
		0xAB	Progress	BIOS Setup Utility input wait
		0xAD	Progress	Ready to boot event
	Operating system phase	0xAA	Progress	APIC mode
		0xAC	Progress	PIC mode

Notices

Federal Communications Commission Statement

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- This device may not cause harmful interference.
- This device must accept any interference received including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

The use of shielded cables for connection of the monitor to the graphics card is required to assure compliance with FCC regulations. Changes or modifications to this unit not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

Compliance Statement of Innovation, Science and Economic Development Canada (ISED)

This device complies with Innovation, Science and Economic Development Canada licence exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

CAN ICES-003(A)/NMB-003(A)

Déclaration de conformité de Innovation, Sciences et Développement économique Canada (ISED)

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CAN ICES-003(A)/NMB-003(A)

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Please refer to https://csr.asus.com/Compliance.htm for information disclosure based on regulation requirements ASUS is complied with:

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Các sản phẩm ASUS bán tại Việt Nam, vào ngày 23 tháng 9 năm2011 trở về sau, đều phải đáp ứng các yêu cầu của Thông tư 30/2011/TT-BCT của Việt Nam.

Türkiye RoHS

AEEE Yönetmeliğine Uygundur

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R-R-MSQ-ESC-N8A-E12

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Accessories that came with this product have been designed and verified for the use in connection with this product. Never use accessories for other products to prevent the risk of electric shock or fire.

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